Concurrent Systems

Nebenläufige Systeme

VII. Semaphore

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Preface

Fundamentals
  Classification
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Implementation
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  Mutex

Summary
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- with the **general semaphore** as a measure that supports both
- while the **binary semaphore** was/is intended to support the latter, only
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- but one have to be much more precise and argue with caution as far as the binary alternative is concerned
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*A **binary semaphore** is a valid implementation of one of the many “mutex methods”, but not that restrictive as a “mutex object” need to be.*
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A **binary semaphore** is a valid implementation of one of the many “mutex methods”, but not that restrictive as a “mutex object” need to be.

elaboration of various implementation aspects regarding both types of semaphore as well as mutex as an object
Colloquialism

(Ger.) Gemeinsprache, Redensart

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(Gr.) sêma·pherein,  
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Preface
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The semaphores are essentially non-negative integers; when only used to solve the mutual exclusion problem, the range of their values will even be restricted to “0” and “1”. [2, p. 28]
Concept for Cooperation and Communication

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### Definition (General Semaphor)

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- also referred to as **counting semaphore** (Ger. *zählender Semaphor*)
insensitive to the distinction between binary and general semaphore is the definition of two **intrinsic primitives** [1]:

**P** abbr. for (Hol.) *prolaag*; a.k.a. *down*, *wait*, or *acquire*, resp.

**V** abbr. for (Hol.) *verhoog*; a.k.a. *up*, *signal*, or *release*, resp.
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**P** abbr. for (Hol.) *prolaag*; a.k.a. *down*, *wait*, or *acquire*, resp.
- decreases\(^1\) the value of the semaphore by 1:
  - i. iff the resulting value would be non-negative [2, p. 29]
  - ii. non-constraining [3, p. 345]
- blocks the process iff the value is or was, resp., 0 before decrease
  - blocking processes are put on a **waitlist** associated with each semaphore

\(^{1}\)This does not only mean subtraction or addition, resp., in arithmetical terms.
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**Hint (Waitlist)**

*The queuing discipline rivals with planning decisions of the process scheduler and, thus, may be the cause of critical interference.*

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multilateral synchronisation [5, p. 15] of interacting processes

- the critical section is considered as a non-preemptable reusable resource that needs to be allocated indivisibly to a process to be usable correctly
- in logical respect, the process having completed $P$ on semaphore $S$ is the only one being authorised to complete $V$ on $S$

```c
semaphore_t mutex = {1};

{
    P(&mutex);
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**Binary Semaphore**

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**Hint (Mutex (cf. p. 14/15))**

A mutex is a binary semaphore that incorporates an explicit check for authorisation to release a critical section in the moment of $V$. 
unilateral synchronisation [5, p. 15] of interacting processes

- used for availability control of entities of the following resource types:
  1. a consumable resource in the form of any data of any number
  2. a reusable resource of limited number, e.g., a data store (buffer), any device

- typical for, but not limited to, producer/consumer systems
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also as noted previously [5, p. 15], this art of synchronisation means:

- logical
  - coordination as indicated by a particular “role playing”
  - e.g., in order to proceed, a “data consumer” depends on the data to be made available by a “data producer”
**General Semaphore**

**Functional Dedicated**

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    - e.g., in order to proceed, a “data consumer” depends on the data to be made available by a “data producer”
  - **conditional**
    - coordination as indicated by a condition for making progress
    - e.g., in order to proceed, a “data producer” depends on the store available for data handling
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  - e.g., in order to proceed, a “data producer” depends on the
store available for data handling
  - in the end, the data store will have to be deallocated and,
thus, made available again by the “data consumer”

from this it follows that $P$ and $V$ applied to the same semaphore $S$
must have to be accomplishable by different processes, normally
- which makes the big difference to a binary semaphore or mutex, resp.
Consumable Resource

semaphore_t data = {0};

void producer() {
  for (;;) {
    /* data released */
    V(&data);
  }
}

void consumer() {
  for (;;) {
    P(&data);
    /* data acquired */
  }
}

- default value is 0
- \( P \) must block out only if there is no data
- \( V \) indicates more data

- calling sequence
  - \( V \) must be actable independent of \( P \)
  - in order to complete, \( P \) depends on \( V \)
  - beware of an overflow of the values margin
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semaphore_t data = {0};

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  - V indicates more data

### calling sequence
- V must be actable independent of P
- in order to complete, P depends on V

→ beware of an **overflow** of the values margin

- usually, producer and consumer are **different** interacting processes
- in case of one and the same process, the number of a completed V must exceed the number of a completed P in order to prevent deadlock
- #V > #P, which implies a path V → P (i.e., V “happens before” P)
semaphore_t store = {N};

void producer() {
    for (;;) {
        P(&store);
        /* store acquired */
    }
}

void consumer() {
    for (;;) {
        /* store released */
        V(&store);
    }
}

- default value is \( N \geq 0 \)
- \( P \) must block out only if there is no store
- \( V \) indicates more store
- calling sequence
  - \( V \) must be actable independent of \( P \)
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Reusable Resource

```c
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        V(&store);
    }
}
```

- default value is $N \geq 0$
- $P$ must block out only if there is no store
- $V$ indicates more store
- calling sequence
  - $V$ must be actable independent of $P$
  - in order to complete, $P$ depends on $V$
  - $\Leftarrow$ beware of an overflow of the values margin

as to interacting processes in the line of producer and consumer, the same applies as mentioned before: $\#V > \#P$

in other cases: $\#V \leq \#P$, must be completed by the same process
Hint (Bounded Buffer)

A means of managing an unlimited number of consumable resources on the basis of a limited number of reusable resources.
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void consumer () {
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        /* data acquired */
        /* store released */
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}
```

indisputable classic in cooperation and communication of processes

- simply a merge of the semaphore use pattern discussed as before
- **transverse application** of $P$ and $V$ to a pair of general semaphores
Checking **authorisation** for release of a critical section in that very moment is improper for a general semaphore, optional for a binary semaphore, and may be **demanded** for a mutex (cf. p. 15).
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Checking **authorisation** for release of a critical section in that very moment is improper for a general semaphore, optional for a binary semaphore, and may be demanded for a mutex (cf. p. 15).

demanded

- a **mutex object** ensures that the release of critical section $CS$ will succeed only for the process having acquired $CS$.
- by extending a binary semaphore, $P$ will have to record and $V$ will have to check ownership of $CS$. 
Checking **authorisation** for release of a critical section in that very moment is improper for a general semaphore, optional for a binary semaphore, and may be demanded for a mutex (cf. p. 15).

**improper**

- $P$ and $V$ on a **general semaphore** must be accomplishable in particular also by different processes
- this is prevented by a mutex **object**—but not by a mutex
Checking **authorisation** for release of a critical section in that very moment is improper for a general semaphore, optional for a binary semaphore, and may be demanded for a mutex (cf. p. 15).

- **optional**  
  - basically, a **binary semaphore** may be implemented by a general semaphore $S$, with $S \leq 1 \Rightarrow$ never a mutex object  
  - values $S > 1$ must be prevented either by the use pattern or by the implementation of $P$ and $V$
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- if **authorisation fails**, the process attempting to release $CS$ should be aborted.
Semaphore v. Mutex I

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if **authorisation fails**, the process attempting to release CS should be aborted—in kernel mode, the computing system must be halted...
Hint (Computer Science Folklore)

A *semaphore* can be released by *any* process.
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A semaphore can be released by any process.

Incomplete or rough, if not broad-bush, phrase that must be regarded with suspicion—one have to distinguish between semaphore types.

- Strictly, essence of this phrase is requirement for a general semaphore.
- Strictly as well, it is merely an option for a binary semaphore.
  - In logical respect, a binary semaphore cannot be released by any process.
  - In physical respect, this however is not a must for any implementation.

\(^2\)see also p. 36
Semaphore v. Mutex II

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A mutex can be released only by the process having it acquired.

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Semaphore v. Mutex II

Conceptual Level

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Hint (Computer Science Folklore)

A mutex can be released only by the process having it acquired.

- a phrase that is slanted towards only one aspect as to the leastwise twofold non-uniform common understanding about a mutex:
  1. a category of methods for ensuring mutual exclusion or
  2. the implementation of one of these methods in terms of an object.

2see also p. 36
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- if so, both may also entail context switching—“may” because:

  - if no further process is ready to run, the **idle loop** becomes active
    - in that case, the blocking process likewise may fade to the **idle process**
    - thus, doing **without** a dedicated **idle-process instance** and context switch
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- $V$ – if there is a waiting process, it will be set “ready to run” (cf. [9, p. 28])
  - in that case, **priority violation** must be prevented (scheduling discipline!)
  - thus, the current process may defer to a prior-ranking one: context switch

---

The standby position of a process within $P$ is passive, normally. If a process is blocked or unblocked, it may entail rescheduling. If so, both may also entail context switching. If there is a waiting process, it will be set “ready to run” if it is of higher priority than the current process. This must be prevented by ensuring that the process is not scheduled to run. Context switching may occur in this scenario.
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- all this makes $P$ and $V$ programs of the operating system machine level

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- all this makes $P$ and $V$ programs of the operating system machine level
- $P$ and $V$ relies on **process management** of the operating system
- one have to put the current process asleep and get a sleeping process up

---

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  - if no further process is ready to run, the **idle loop** becomes active
  - in that case, the blocking process likewise may fade to the **idle process**
  - thus, doing **without** a dedicated **idle-process instance** and context switch
  
  **V** – if there is a waiting process, it will be set “ready to run” (cf. [9, p. 28])
  - in that case, **priority violation**\(^3\) must be prevented (scheduling discipline!)
  - thus, the current process may defer to a prior-ranking one: context switch
  
**P** and **V** programs of the operating system machine level

**P** and **V** relies on **process management** of the operating system

- one have to put the current process asleep and get a sleeping process up
- in functional terms, however, **P** and **V** need not be system calls
- in non-functional terms, **P** and **V** should be close to the **scheduler**
  - by settling **P** and **V** in the address space of the operating-system kernel **or**
  - by making scheduler functions available through “strawweight” system calls

\(^3\)If at least one of the processes on the waitlist is of higher-priority than the current process but will not become “ready to run” or allocated the processor.
in order to aid $V$, processes blocked by $P$ at a semaphore are entered on a waitlist in either logical or physical means
in order to aid \( V \), processes blocked by \( P \) at a semaphore are entered on a waitlist in either logical or physical means

**Logical**
- to block, a **blocked-on mark** is stored in the process descriptor
- to unblock, a process-table walk looks for that mark
  - constant (\( P \)) and variable but bounded above (\( V \)) run-time
  - blocked-on mark is a “magic” address, no extra attributes
Waitlist Association

- in order to aid $V$, processes blocked by $P$ at a semaphore are entered on a waitlist in either logical or physical means

**physical**
- to block, the process descriptor joins a *queue data structure*
- to unblock, a process descriptor is removed from that structure
  - variable but bounded above ($P$) and constant ($V$) run-time
  - additional queue attribute of the semaphore data structure
in order to aid \( V \), processes blocked by \( P \) at a semaphore are entered on a waitlist in either logical or physical means

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It is desirable to have the waitlist queuing discipline in compliance with the process scheduling discipline: **freedom of interference**
- a characteristic by means of which **priority violation** will be prevented
- usually, this excludes straightforward queuing disciplines such as FCFS
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**Hint (Process-Table Walk—Conformance to Scheduling)**

*Part of the scheduler, lookup function to locate a process descriptor on the basis of the blocked-on mark as search key.*
In the absence of simultaneous processes, the implementation of a semaphore could be as simple as follows:

```c
void prolaag(semaphore_t *sema) {
    if (!claim(sema)) /* at the moment, unavailable */
        sleep(&sema->wand);
}

void verhoog(semaphore_t *sema) {
    if (unban(sema)) /* as from now, available */
        rouse(&sema->wand);
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```

Whereat `claim` decreases and `unban` increases the value of the semaphore according to binary or general, resp., characteristic.

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```

whereat `claim` decreases and `unban` increases the value of the semaphore according to binary or general, resp., characteristic\(^4\)

but, assuming that the presence of simultaneous processes is possible, this implementation shows a race condition \(\leadsto\) lost wakeup

\(^3\) while going to sleep, i.e. being “sleepy”, the process gets delayed
\(^7\)\(^–\)\(^8\) but in good faith of a sleeper, the “sleepy” process may be missed

\(^4\)The implementation of these helper functions will be revealed later.
$P$ and $V$ itself constitute a **critical section**, likewise, that must be protected in order to function correctly

- protection should be constructed **per semaphore instance**, not $P/V$

```c
void prolaag(semaphore_t *sema) {
    atomic *sema = {
        if (!claim(sema))
            sleep(&sema->wand);
    }
}

void verhoog(semaphore_t *sema) {
    atomic *sema = {
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            rouse(&sema->wand);
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}
```
**Indivisible Operation II**

**Scope**

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- As a process will have to block inside a critical section, **deregulation of protection** is indispensable for the period the process is blocked.
Indivisible Operation II

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}
```

Deadlock Prevention

Provided that protection of the critical section on the $P$ side is not deregulated, the $V$ side will never complete and, thus, will never cause unblocking of a process:

- the right location for deregulation is `sleep`
- after the process was marked sleeping

as a process will have to block inside a critical section, **deregulation of protection** is indispensable for the period the process is blocked
protection of the $P/V$ pair against simultaneous processes sharing a semaphore follows either the blocking or non-blocking paradigm

**blocking**
- inhibit FLIH\(^5\), postpone SLIH\(^5\), or lock process
- problem-specific construction of an *enter/leave* pair

**non-blocking**
- fall back on the elementary operations of the ISA level
- problem-specific construction of $P$ and $V$

\(^5\) abbr. for *first- or second-level interrupt handling*, resp.
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Shallows

Implementation

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- more detailed analysis of the “atomic” version of $P$ reveals another problem: **overtaking** of an aroused process
  - upon return from sleep a formerly blocked process may complete $P$ by mistake, joining a process in the critical section to be protected by $P$
  - note that completion of $V$ also opens the door for any process, not only for a process having been blocked at the semaphore
    - aroused processes will have to **retry claiming**: if $\rightarrow$ while

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  ↩ aroused processes will have to **retry claiming**: if ↩ **while**

not least, concurrency had to be constricted to no more than what is absolutely necessary: reflect on *claim/sleep* and *unban/rouse*

\(^5\) abbr. for *first- or second-level interrupt handling*, resp.
Outline

Preface

Fundamentals
   Classification
   Characteristics

Implementation
   Data Structures
   Functions
   Mutex

Summary
Semaphore Data Type

```c
typedef volatile struct semaphore {
    int gate;       /* value: binary or general */
    wand_t wand;    /* protective shield */
} semaphore_t;
```

The purpose of “wand” (Ger. Zauberstab) is to safeguard the semaphore operations in various respects. It protects P and V against simultaneous processes and gives leeway for protection variants (cf. p.20).

A wand that takes care of mutual exclusion techniques as presented in the previous lecture could be the following:

```c
typedef volatile struct wand {
    lock_t clue;   /* protects P or V, resp. */
    event_t wait;  /* list of sleeping processes */
} wand_t;
```

Becoming acquainted with other wands is the content of future lectures...
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    enter(&sema->wand); /* avert overlapped P or V */
    while (!claim(sema)) /* acquire semaphore */
        sleep(&sema->wand); /* await wakeup signal */
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}

void verhoog(semaphore_t *sema) {
    enter(&sema->wand); /* avert overlapped P or V */
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- exercise caution in the analysis of these program statements:
  3–4 ■ takes care of the overtaking-problem as to aroused processes
  10–11 ■ in case of (i) logical waitlist and (ii) strict binary semaphore, the
  search for sleeping processes happens unconditionally
  ■ in that particular case, there is no direct indication of sleepers
Acquire and Release Semaphore

load/store-based implementation for a binary semaphore:

```c
inline bool claim(sema_t *sema) {
    return (sema->gate == 0) ? false : (sema->gate = 1);
}

inline bool unban(sema_t *sema) {
    return !(sema->gate = 0);
}
```

enumerator-based implementation for a general semaphore:

```c
inline bool claim(sema_t *sema) {
    return sema->gate -- > 0;
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inline bool unban(sema_t *sema) {
    return (sema->gate ++ < 0);
}
```

note that both variants are sensitive to simultaneous processes use within a safeguarded program section is assumed...
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note that both variants are sensitive to simultaneous processes use within a safeguarded program section is assumed...
Special Process Management

Prevent Lost Wakeup

```c
inline void sleep(wand_t *wand) {
    catch(&wand->wait); /* disclose process to V */
    leave(wand);       /* allow P or V */
    coast();           /* take a break */
    enter(wand);       /* apply for return to P */
}

inline void rouse(wand_t *wand) {
    leave(wand);       /* allow P or V */
    cause(&wand->wait); /* signal end of break */
}
```
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constrict concurrency to no more than what is absolutely necessary:
  2  ■ endorse interest of the current process of upcoming dormancy
  3  ■ soon dormant process was made known, deregulate P safeguard
  4  ■ transition to dormant state: rescheduling, context switch or idleness
  5  ■ apply for return to safeguarded P
  9  ■ dormant processes could be available, deregulate V safeguard
 10 ■ annulment of dormant state: rescheduling, context switch
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General Process Management

Event Handling

catch

exists in two variants, depending on the waitlist model (cf. p17):

i store of a blocked-on mark in the process descriptor

or

ii enqueue of the process descriptor into a queue data structure

variant (i) writes to an own data structure of the current process,

while variant (ii) manipulates a shared data structure
signalises upcoming blocking (dormancy) of the registered process

cost

blocks the current process, reschedules the processor, and either

performs a context switch or runs through the idle loop

manipulates a shared data structure (ready list)

performs the queuing function of the queue-based

catch

eventually returns when the blocking condition was nullified

cause

unblocks the next registered process, if any, found by means of a

(i) process-table walk or (ii) dequeue operation

manipulates a shared data structure (ready list)

if need be, the current process defers to a prior-ranking process

Hint (Idle State (cf. p.16 and p.37))
The last process blocked may find itself on the ready list. Same may
happen to the “sleepy process” as coast runs deregulated to P/V.
**General Process Management**

**Event Handling**

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- eventually returns when the blocking condition was nullified

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CS (WS 2015, LEC 7) Implementation – Functions
General Process Management

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**Hint (Idle State (cf. p.16 and p.37))**

The last process blocked may find itself on the ready list. Same may happen to the “sleepy process” as coast runs deregulated to P/V.
Process States and State Transitions

cf. [9, p. 27]

- ready ↔ running
- blocked → ready
- running → blocked

- scheduler
- iff effective signalling \((V)\), i.e., waiting process
- \(P\), intermediate step needed: prevent lost wakeup

Ready \(\leftrightarrow\) Running

Blocked \(\rightarrow\) Ready

Running \(\rightarrow\) Blocked

Scheduler

Effective Signalling

Waiting Process

Intermediate Step

Prevent Lost Wakeup
Process States and State Transitions
cf. [9, p. 27]

- ready ↔ running
- blocked → ready
- running ↔ pending
- pending → blocked

- scheduler
- iff effective signalling \((V)\), i.e., waiting process
- doze \((P \rightarrow)\), effective signalling \((\leftarrow V)\)
- deep sleep \((P)\), no overlapping \(V\)
as there is no single solution to protect $P$ and $V$ adequately, the wand attribute symbolises intention to application orientation

- depending on the mode of operation or use case, the wand acts differently
- assuming that processing elements are not multiplexed [7, p. 5], then:

```c
inline void enter(wand_t *wand) {
    lock(&wand->clue);
}

inline void leave(wand_t *wand) {
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Semaphore Gatekeeper

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wand capability depends on the “type of exclusion” in relation to the required characteristics of the operating system machine level:

- **partial**
  - processor **multiplexing** \( \sim \) interrupt control
- **mutual**
  - processor **multiplication** \( \sim \) process lock, see example above
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- **partial** processor **multiplexing** $\leadsto$ interrupt control
- **mutual** processor **multiplication** $\leadsto$ process lock, see example above

combination of both is optional, not mandatory, and problem-specific

depends on the degree of parallelism (a) allowed for by the application use case and (b) made possible by the ISA level
## Extent of Critical Section

Binary Semaphore devoid of Waitlist

---

1. movl 16(% esp ), %edi
2. leal 4(% edi ), %esi
3. jmp LBB0_2
4. LBB0_1 :
   5. movl _life, %eax
   6. movl %esi, 4(% eax)
   7. movl %esi, (% esp)
   8. calll _unlock
   9. calll _coast
10. LBB0_2 :
    11. movl %esi, (% esp)
    12. calll _lock
    13. cmpl $0, (% edi)
    14. je LBB0_1
    15. movl $1, (% edi)
    16. movl %esi, (% esp)
    17. calll _unlock
    18. ...

Let the sequence of instructions within \( P \) be as follows:

1. point at semaphore
2. point at lock structure
3. address is blocked-on mark
4. apply for \( P \) protection
5. occupied, \( S \) already taken
6. point at process structure
7. define blocked-on mark
8. deregulate \( P \) protection
9. fall asleep, dream about


locking overhead when unoccupied

non-blocking synchronisation

Take a sledgehammer to crack a nut...
let the sequence of instructions within $P$ be as follows:

2. point at semaphore
3. point at lock structure
   - address is blocked-on mark
12–13. apply for $P$ protection
14–15. check binary semaphore $S_b$
16–18. unoccupied, take $S_b$
   - quit $P$ protection, done
5. occupied, $S_b$ already taken
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8–9. deregulate $P$ protection
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Extent of Critical Section

Binary Semaphore devoid of Waitlist

let the sequence of instructions within \( P \) be as follows:

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8. deregulate \( P \) protection
9. fall asleep, dream about \( V \)
10. locking overhead when unoccupied
11. net worth of about 5 instructions
12. non-blocking synchronisation

\[ \text{movl } 16(\%\text{esp}), \%\text{edi} \]
\[ \text{leal } 4(\%\text{edi}), \%\text{esi} \]
\[ \text{jmp LBB0}_2 \]

LBB0_1:

\[ \text{movl } _\text{life}, \%\text{eax} \]
\[ \text{movl } \%\text{esi}, 4(\%\text{eax}) \]
\[ \text{movl } \%\text{esi}, (\%\text{esp}) \]
\[ \text{calll } _\text{unlock} \]
\[ \text{calll } _\text{coast} \]

LBB0_2:

\[ \text{movl } \%\text{esi}, (\%\text{esp}) \]
\[ \text{calll } _\text{lock} \]
\[ \text{cmpl } $0, (\%\text{edi}) \]
\[ \text{je LBB0}_1 \]
\[ \text{movl } $1, (\%\text{edi}) \]
\[ \text{movl } \%\text{esi}, (\%\text{esp}) \]
\[ \text{calll } _\text{unlock} \]

...
given the concept of a binary semaphore, implementation of a mutex is straightforward and, absolutely, no black magic:

- a mutex data structure is composed of two parts:
  - a binary semaphore used to actually protect the critical section and
  - a handle that uniquely identifies the process having acquired the mutex

\[\text{typedef volatile struct mutex} \]
\[\text{\{ semaphore_t sema ;\}}\]
\[\text{\{ process_t * link ;\}}\]
\[\text{\}} \]

\[\text{\} \]

\[7\text{At kernel level, the handle is the pointer to the process descriptor of the process instance. At user level, it is the process identification.}\]
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  - i. a binary semaphore used to actually protect the critical section \textit{and}
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- given such a structure, let the following two functions be defined:
  - \texttt{acquire} – performs the \textit{P} and registers the current process as owner
  - \texttt{release} – conditionally unregisters the owner and performs the \textit{V}
    - in case of a wrong owner, the current process or kernel, resp., panics

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a corresponding **data type** may be laid out as follows:

```c
typedef volatile struct mutex {
    semaphore_t sema;      /* binary semaphore */
    process_t *link;       /* owning process or 0 */
} mutex_t;
```

---

7At kernel level, the handle is the pointer to the process descriptor of the process instance. At user level, it is the process identification.
Acquire and Release Mutex

```
extern void panic(char*) __attribute__((noreturn));

void acquire(mutex_t *mutex) {
    P(&mutex->sema);    /* lockout */
    mutex->link = being(ONESELF);  /* register owner */
}

void release(mutex_t *mutex) {
    if (mutex->link != being(ONESELF)) /* it’s not me! */
        panic("unauthorised release of mutex");

    mutex->link = 0;    /* deregister owner */
    V(&mutex->sema);    /* unblock */
}
```
### Acquire and Release Mutex

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- release of a mutex by an unauthorised process is a serious matter
  - presumably, the non-sequential program contains a software fault (bug)
  - returning an error code is no option, as one cannot rely on error checking
  - any other than “raising a non-maskable exception” is a botch job...
Résumé

- fundamental concept for cooperation and communication
- binary and general/counting semaphore, intrinsic primitives \( P \) and \( V \)
- correlation to unilateral and multilateral synchronisation
- differentiation as to mutex (methods v. implementation/object):
  Hint
  
  A binary semaphore is a valid implementation of one of the many "mutex methods", but not that restrictive as a "mutex object" need to be.

- hierarchic placement at operating system machine level
- characteristics important in functional and non-functional terms
- logical or physical waitlist, conformance to the scheduling discipline
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Semaphore v. Mutex III

Commonalities and differences as to their possible **internal states**.

- **general semaphore** $S_g$:
  - positive: $N > 0$ processes will complete $P(S_g)$ without blocking
  - zero: $P(S_g)$ will block the running process on the waitlist of $S_g$
  - negative: $P(S_g)$ will block the running process on the waitlist of $S_g$ and $|N|$ processes are blocked on the waitlist of $S_g$

- **binary semaphore** $S_b$:
  - not taken: exactly one process will complete $P(S_b)$ without blocking
    - the very process becomes **logical owner** of $S_b$
  - taken: $P(S_b)$ will block the running process on the waitlist of $S_b$
    - $V(S_b)$ should be performed only by the logical owner of $S_b$

- **mutex object** $M$: let $A$ be **acquire** and let $R$ be **release**
  - not owned: exactly one process will complete $A(M)$ without blocking
    - the very process becomes **physical owner** of $M$
  - owned: $A(M)$ will block the running process on the waitlist of $M$
    - $R(M)$ can succeed only for the physical owner of $M$
principle pattern of a scheduler function to block a process
called by *coast* (cf. p. 25) and other functions to pause computation

```c
void block() {
    process_t *next, *self = being(ONESELF);

    while (!(next = elect(hoard(READY))))
        relax(); /* no ready to run... */

    if (next != self) { /* must relinquish */
        self->state = BLOCKED; /* vacate processor */
        seize(next); /* resume elected */
    }
    self->state = RUNNING; /* occupy processor */
}
```

4  ■ choose next process to be dispatched to the processor
5  ■ ready list is empty, so the running process fades to the idle process
7  ■ as the case may be, the running process may be allowed to continue:
   i  the idle/running process found itself ready-to-run on the ready list or
   ii the running process, sent to sleep due to *P*, was roused due to *V* (p. 25)