Energy-Aware Computing Systems

Energiebewusste Rechensysteme

IX. Energy-Aware Programming

Timo Hönig

2018-12-19

EA
SY

Recap

- **system software**
  - operating-system level + low-level user space
  - holistic view onto the overall system
- **high-level energy management**
  - implement **higher-level abstractions**
  - exploit **lower-level** building blocks

Recap

- **system software**
  - operating-system level + low-level user space
  - holistic view onto the overall system
- **high-level energy management**
  - implement **higher-level abstractions**
  - exploit **lower-level** building blocks
- **operational** concerns
  - energy as a basic operating-system resource
  - energy **accounting**, **allocating**, and **administering**
- **integration** considerations
  - challenge: interaction between **process** and **power** management
  - energy-aware operating systems: ECOSystem, Cinder, Linux EAS

Agenda

Preface and Terminology

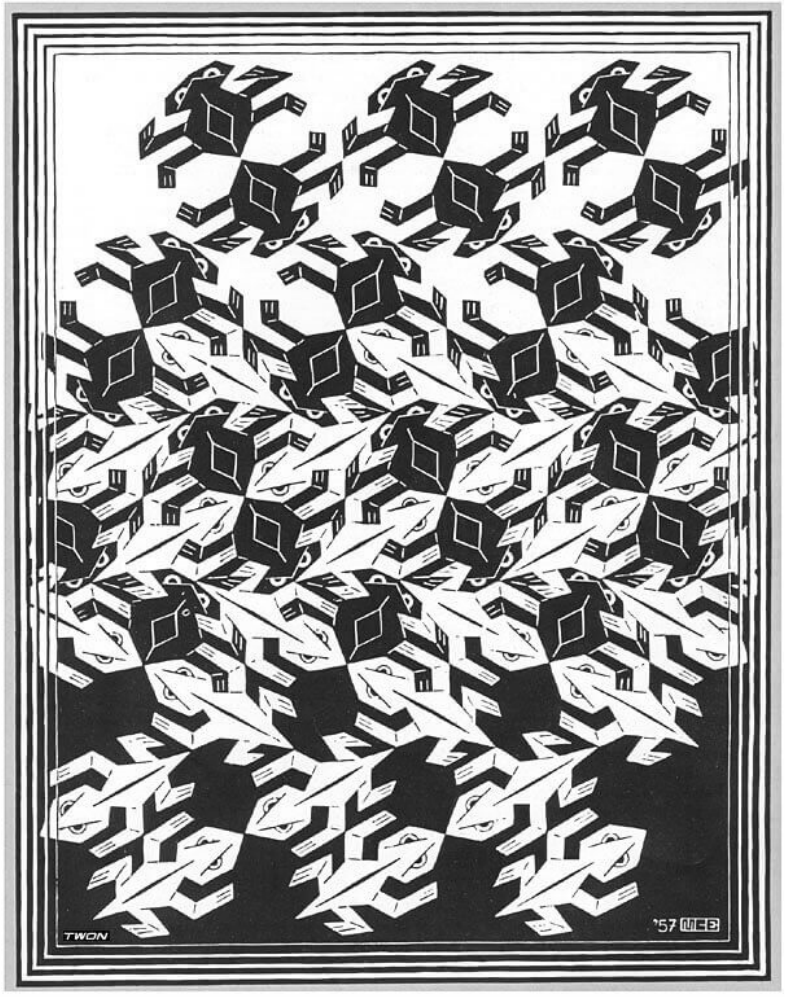
System Activities and Energy Demand
- Cross-Layer Considerations
- Retrospective vs. Prospective

Energy-Aware Programming
- HEAL, ROAM
- Paper Discussion

Summary

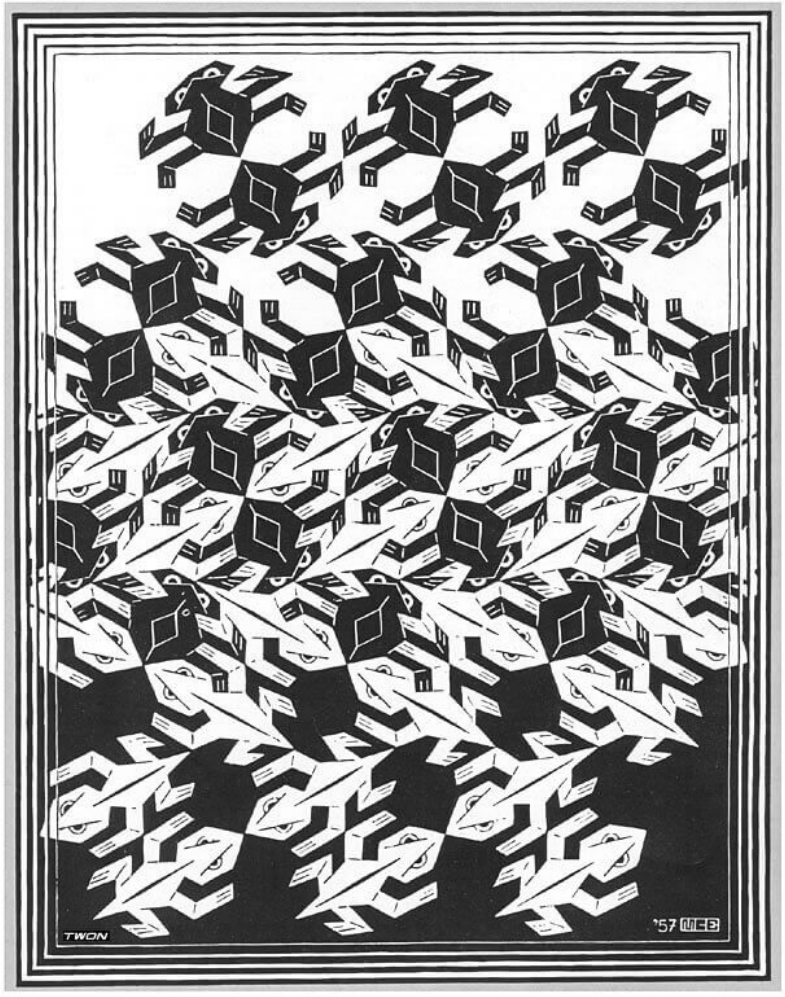
Energy-Aware Programming

- **motivation**
  - knowledge transfer:
    development $\rightarrow$ execution phase
  - reduction of work to
    the necessary minimum
  - carry out the remaining work in the
    most efficient way



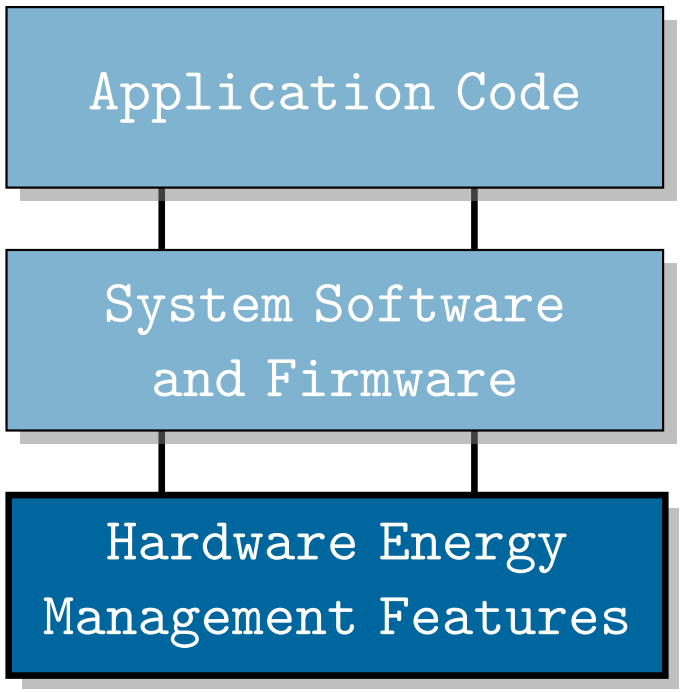
Energy-Aware Programming

- **motivation**
  - knowledge transfer: development → execution phase
  - reduction of work to the necessary minimum
  - carry out the remaining work in the most efficient way
- **operational goals**
  - reduce guesswork by lower system levels (i.e., system software, firmware, and hardware)
  - interweave static aspects (→ ahead of run time) with dynamic aspects (→ at run time)



Agenda

Cross-Layer Considerations



- dynamic voltage and frequency scaling (DVFS)
- sleep states (e.g., CPU C-states, device-specific power saving features)

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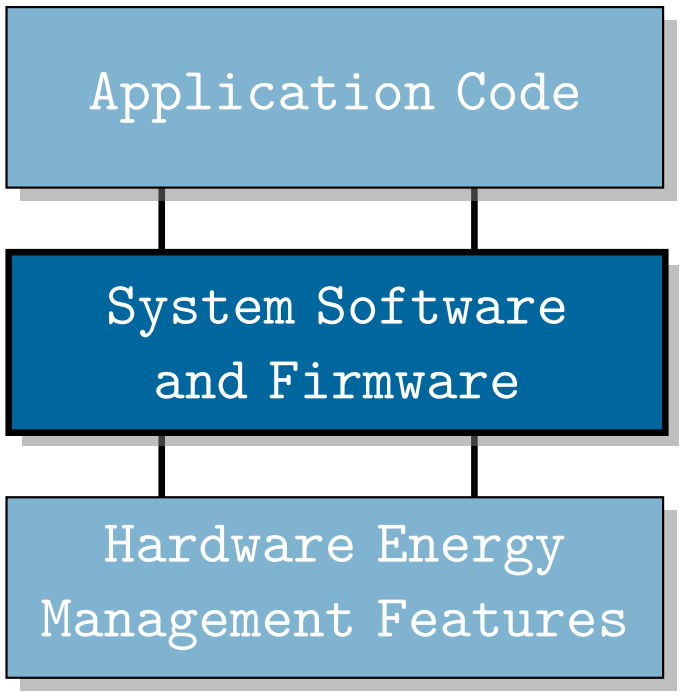

- energy management stack
- latency hiding, race/crawl to sleep

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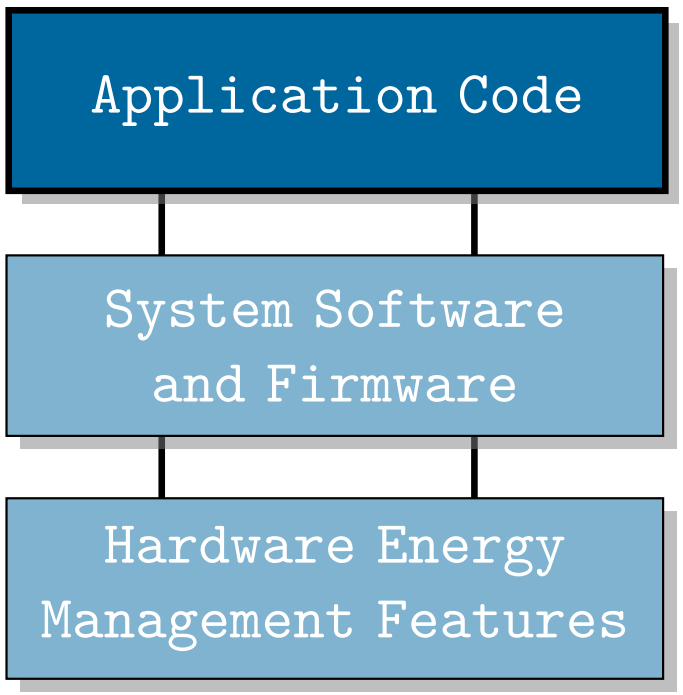

- compiler optimization (e.g., loop optimizations, aligned RAM access)
- tracing and profiling Tools (e.g., PowerTOP)

- energy management stack
- latency hiding, race/crawl to sleep

- dynamic voltage and frequency scaling (DVFS)
- sleep states (e.g., CPU C-states, device-specific power saving features)

Retrospective vs. Prospective: Analysis

- statistics at process level (e.g., PowerTOP), unit of measurement is *wake-ups per second*
- wake-ups cause the CPU to return from C-state, subsequent activities (e.g., I/O) are likely to follow
- less wake-ups $\rightarrow$ lower energy demand

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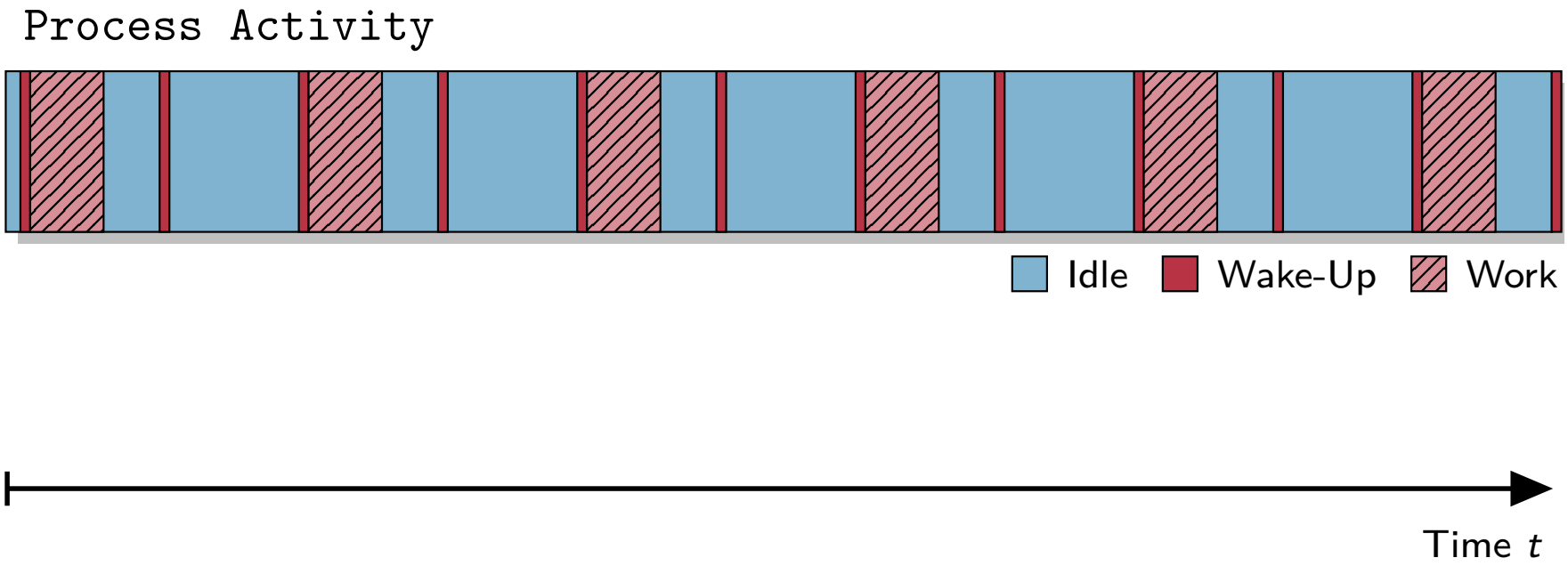

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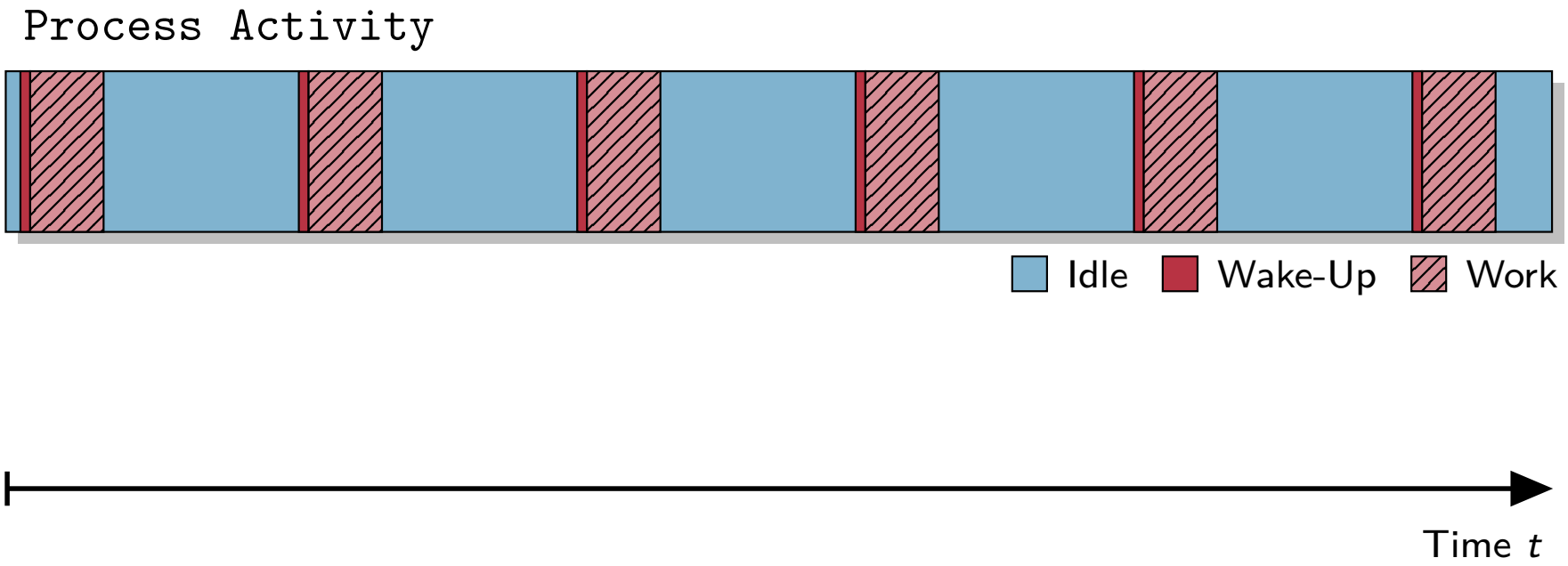

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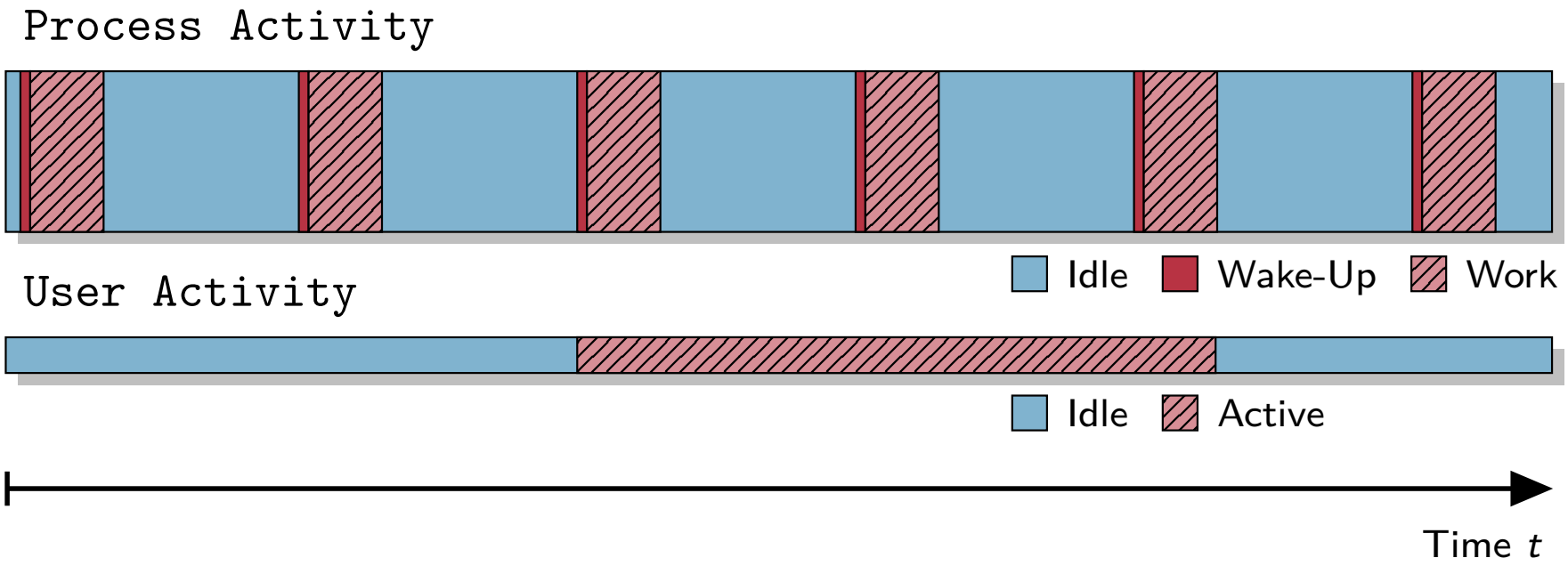

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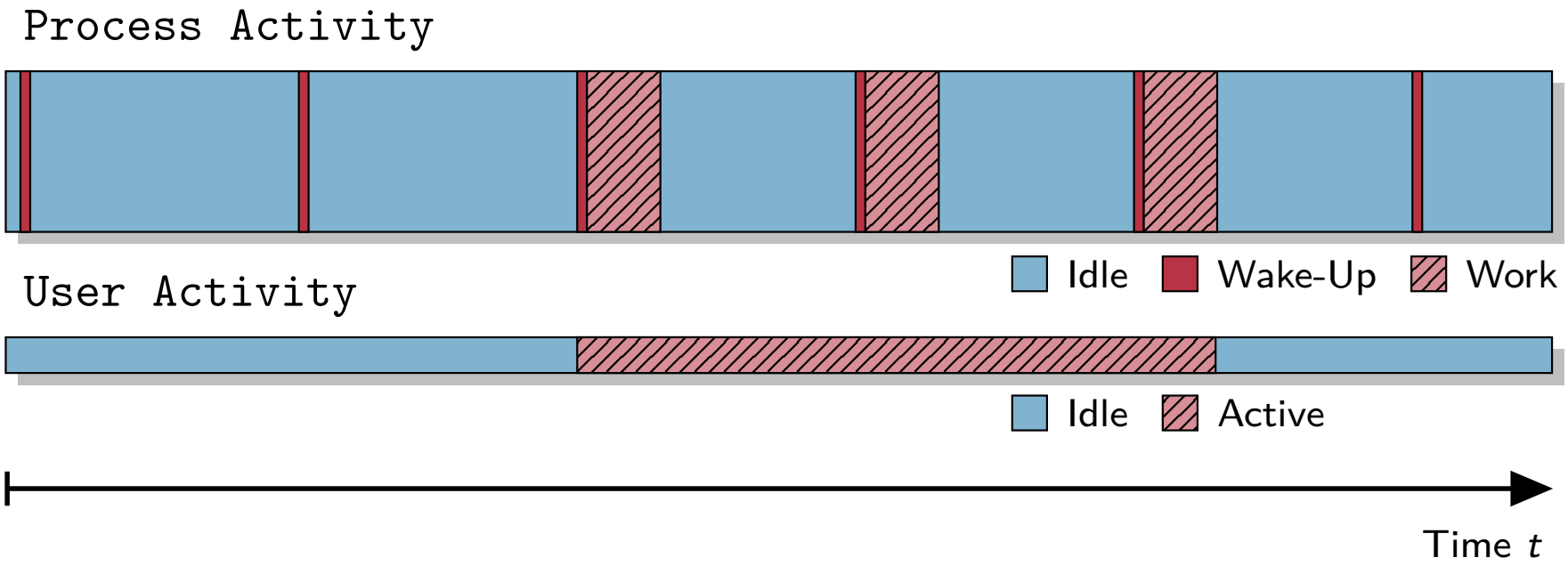
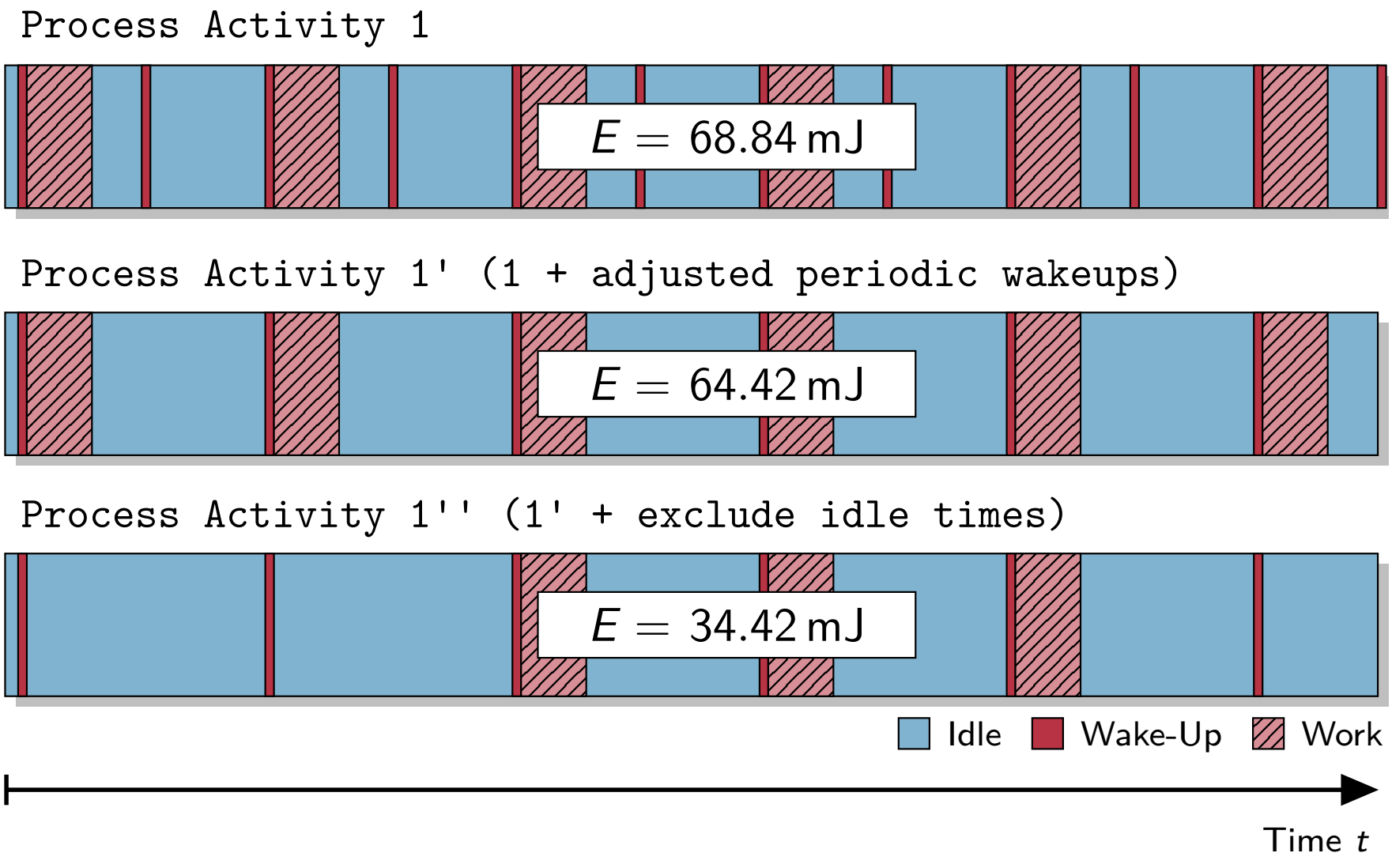
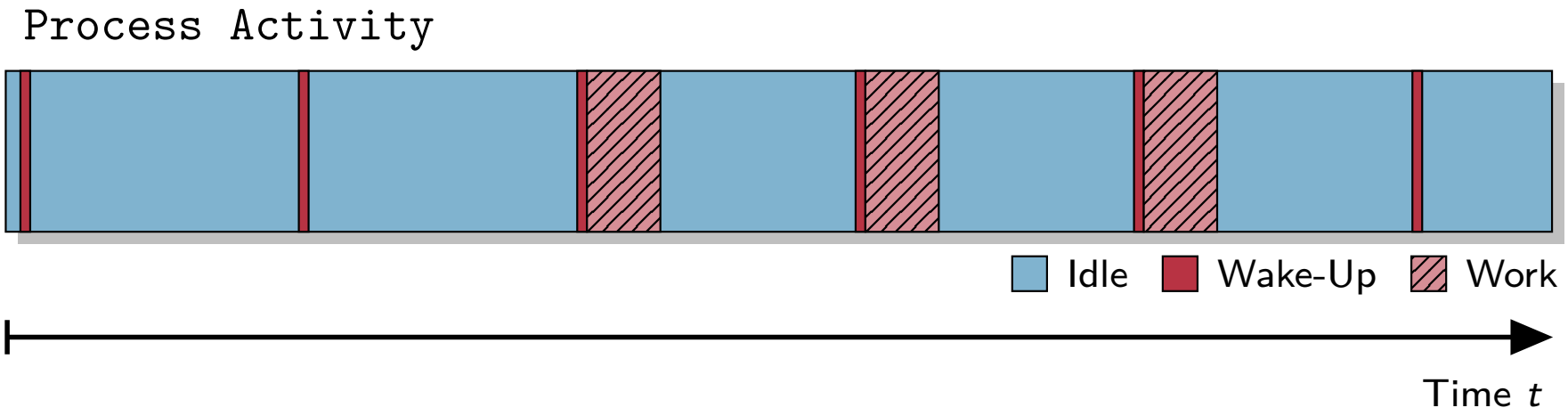
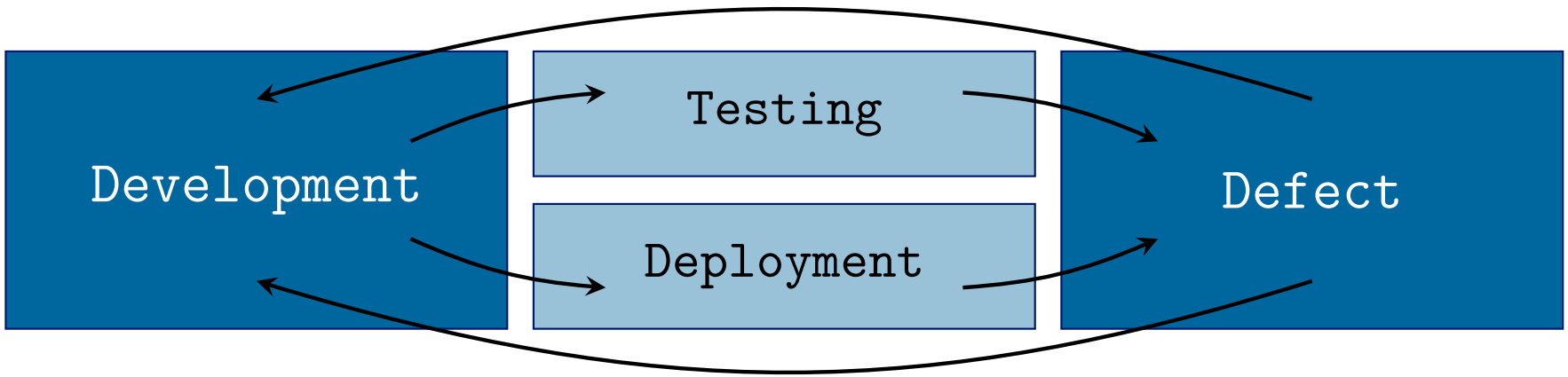


Retrospective vs. Prospective: Revisions and Impact



Retrospective vs. Prospective: Backward-Looking





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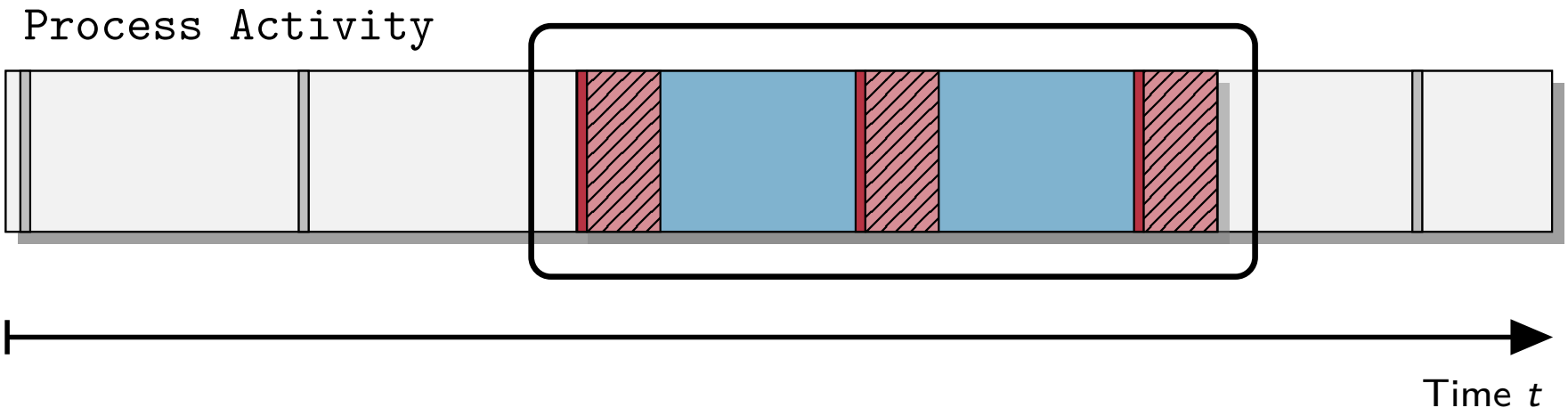

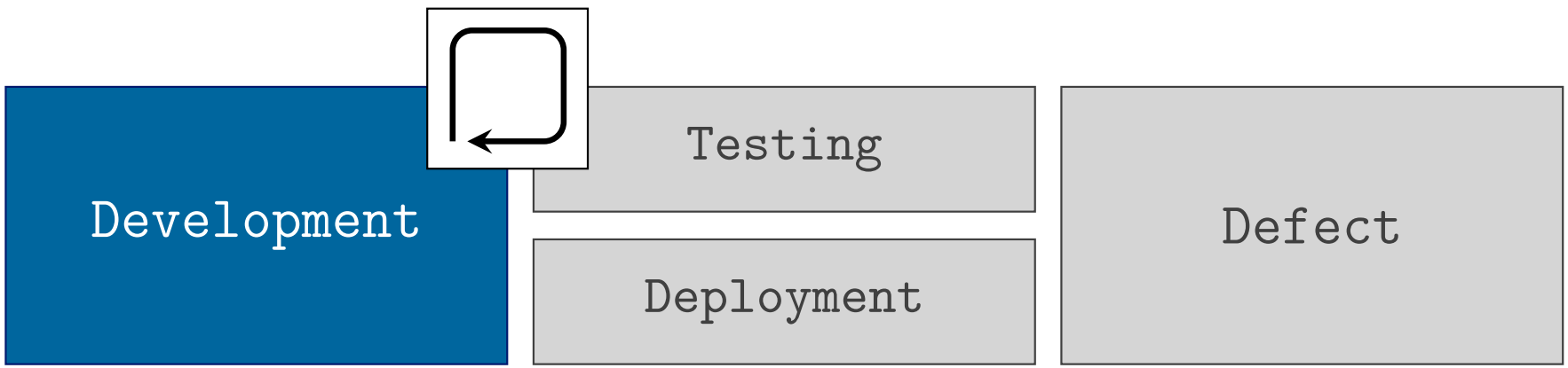

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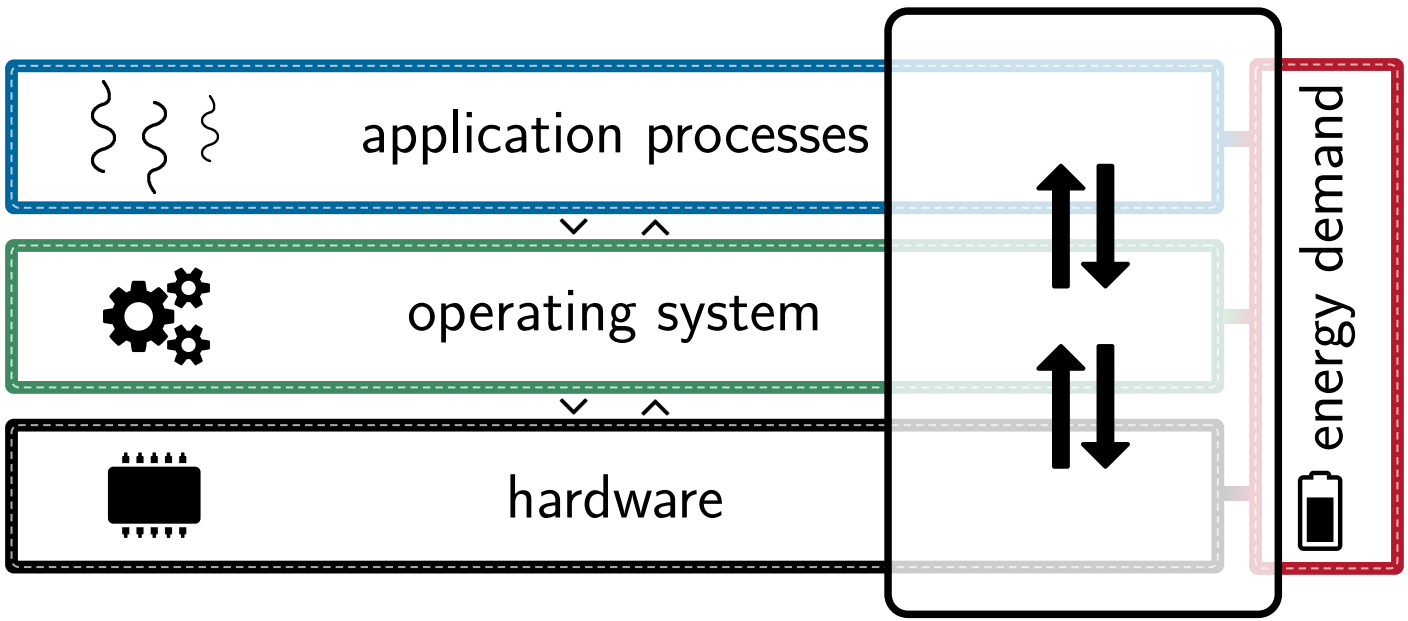
Energy-Aware Programming

- proactive energy-aware computing
  - cross-layer und cross-phase (positioning and momentum)
  - focus: single-chip computing systems and HPC
- holistic analysis and evaluation of software components with regard to their impact on the energy demand of the systems



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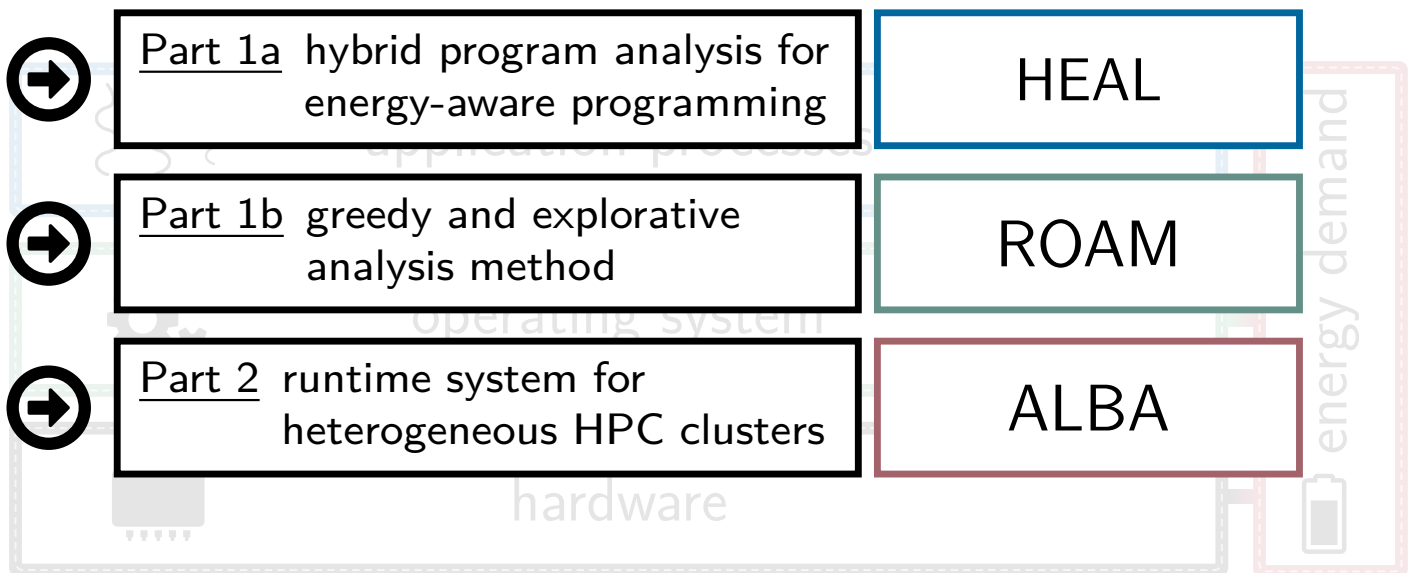


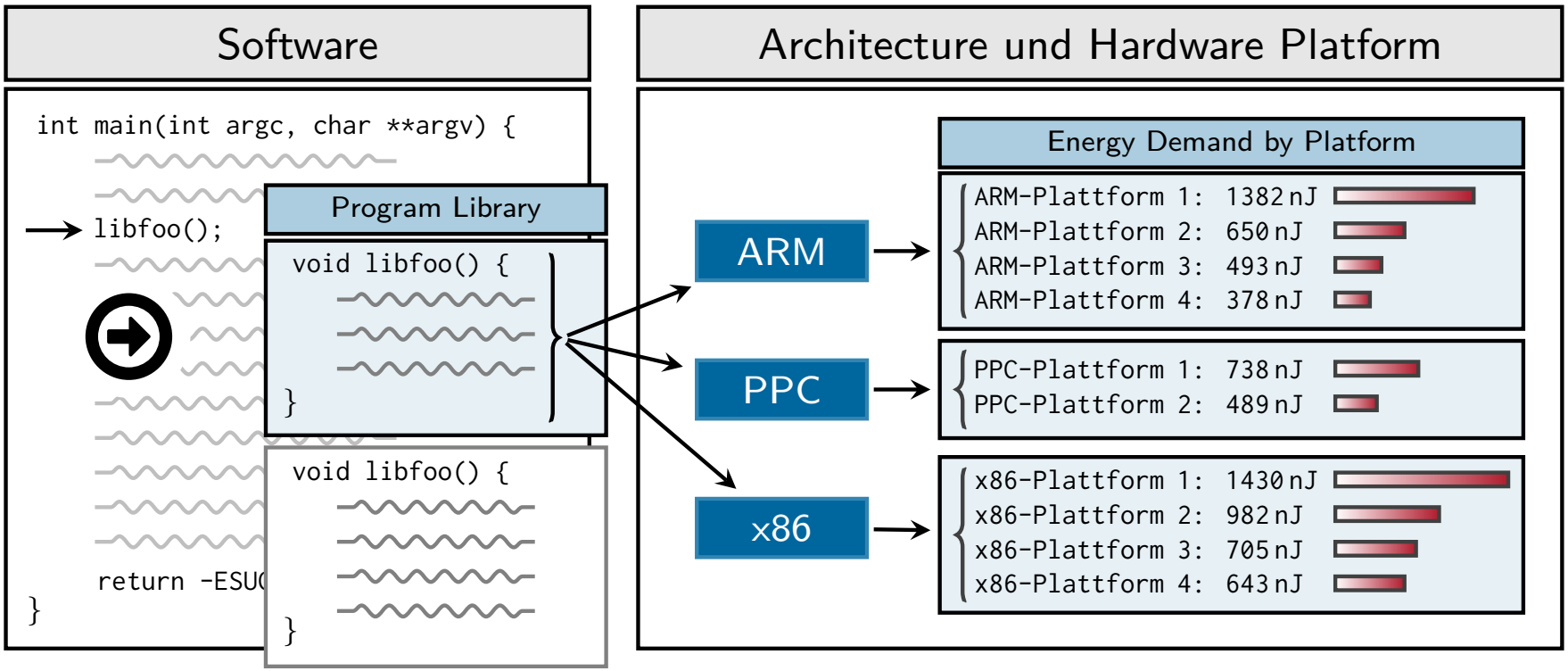
Energy-Aware Programming

ahead of run time

at run time

- Part 1a hybrid program analysis for energy-aware programming → HEAL
- Part 1b greedy and explorative analysis method → ROAM
- Part 2 runtime system for heterogeneous HPC clusters → ALBA

HEAL: Energy-Aware Programming



- making energy demand estimates at the function level available during development
- basis for energy-aware programming decisions

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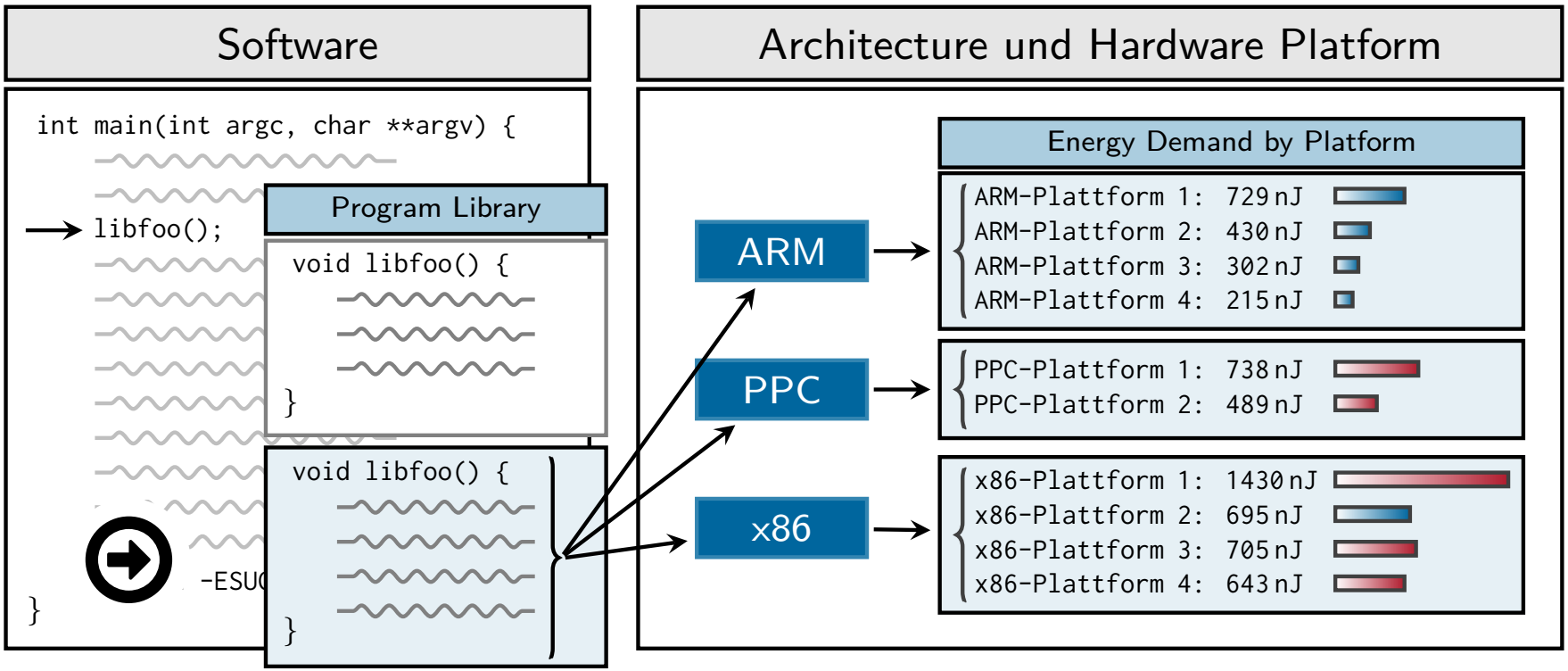

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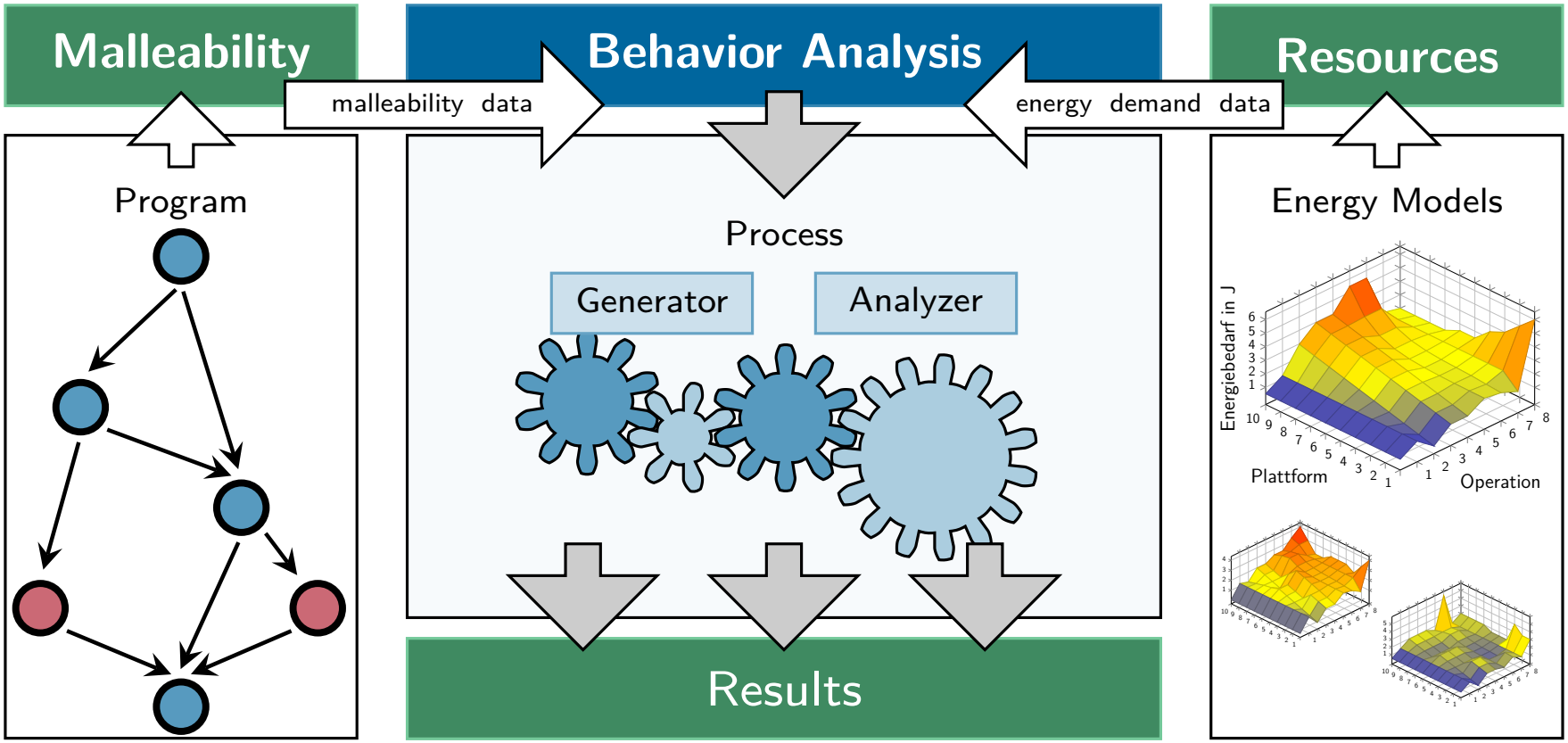
HEAL: Architecture and Implementation



- determine malleability by program analysis
- behavioral analysis with process execution and evaluation
- resource-demand analysis using energy models

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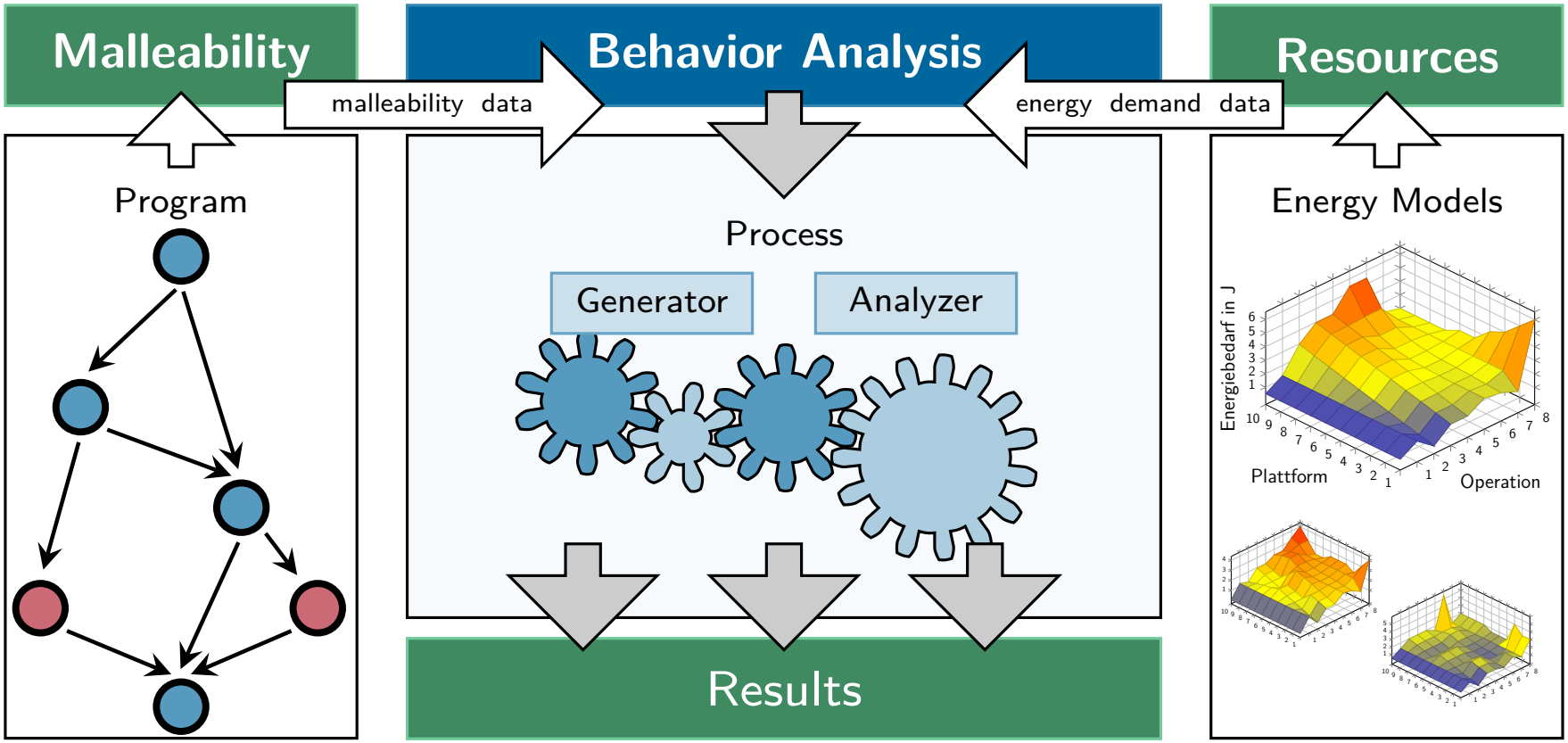

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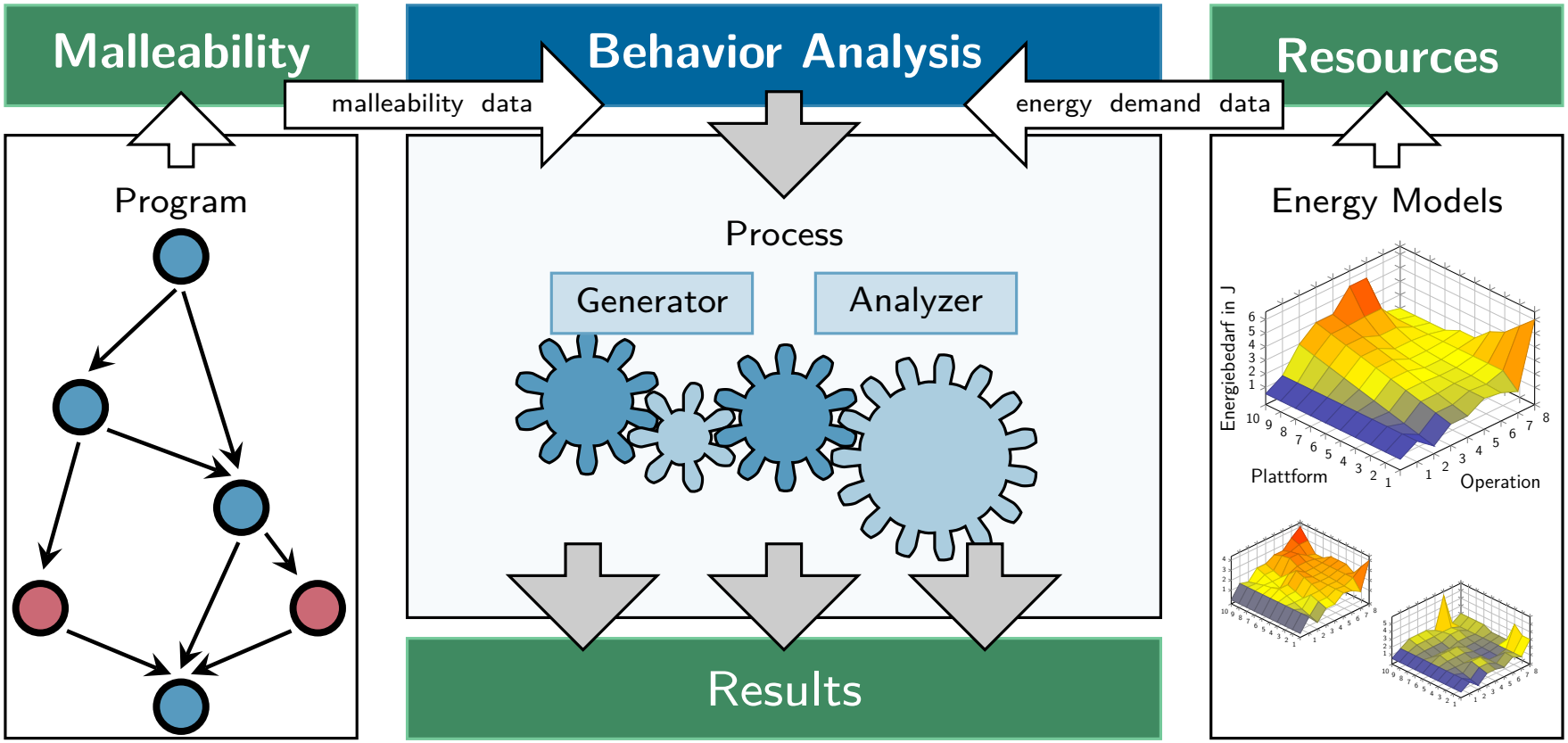

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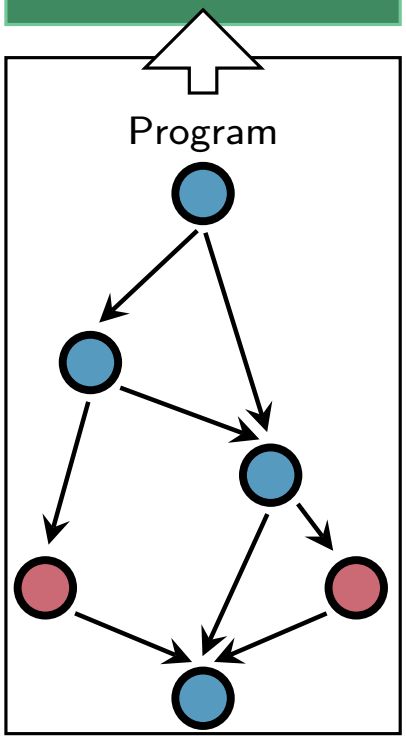
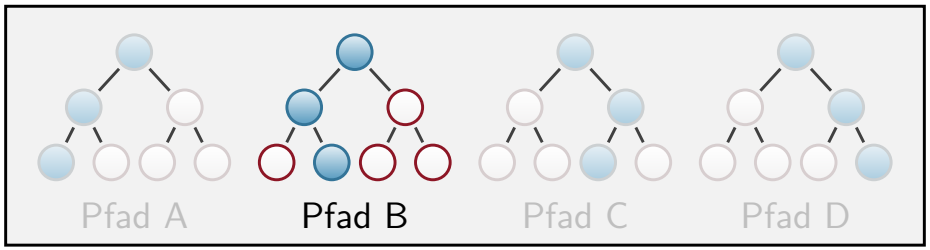
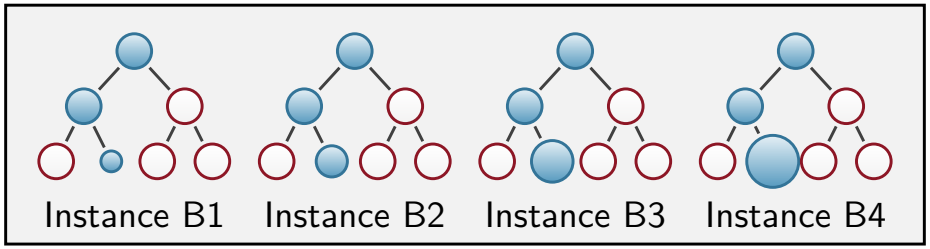
HEAL: Architecture and Implementation

Malleability | Behavior Analysis | Resources




```
 1: if x > 0 then
 2:   if x > 4 then
 3:     ... /* Path A */
 4:   else
 5:     ... /* Path B */
 6:   end if
 7: else
 8:   if x < −16 then
 9:     ... /* Path C */
10:   else
11:     ... /* Path D */
12:   end if
13: end if
```





- determine program paths with symbolic execution
- build stand-alone programs (i.e., with input data)

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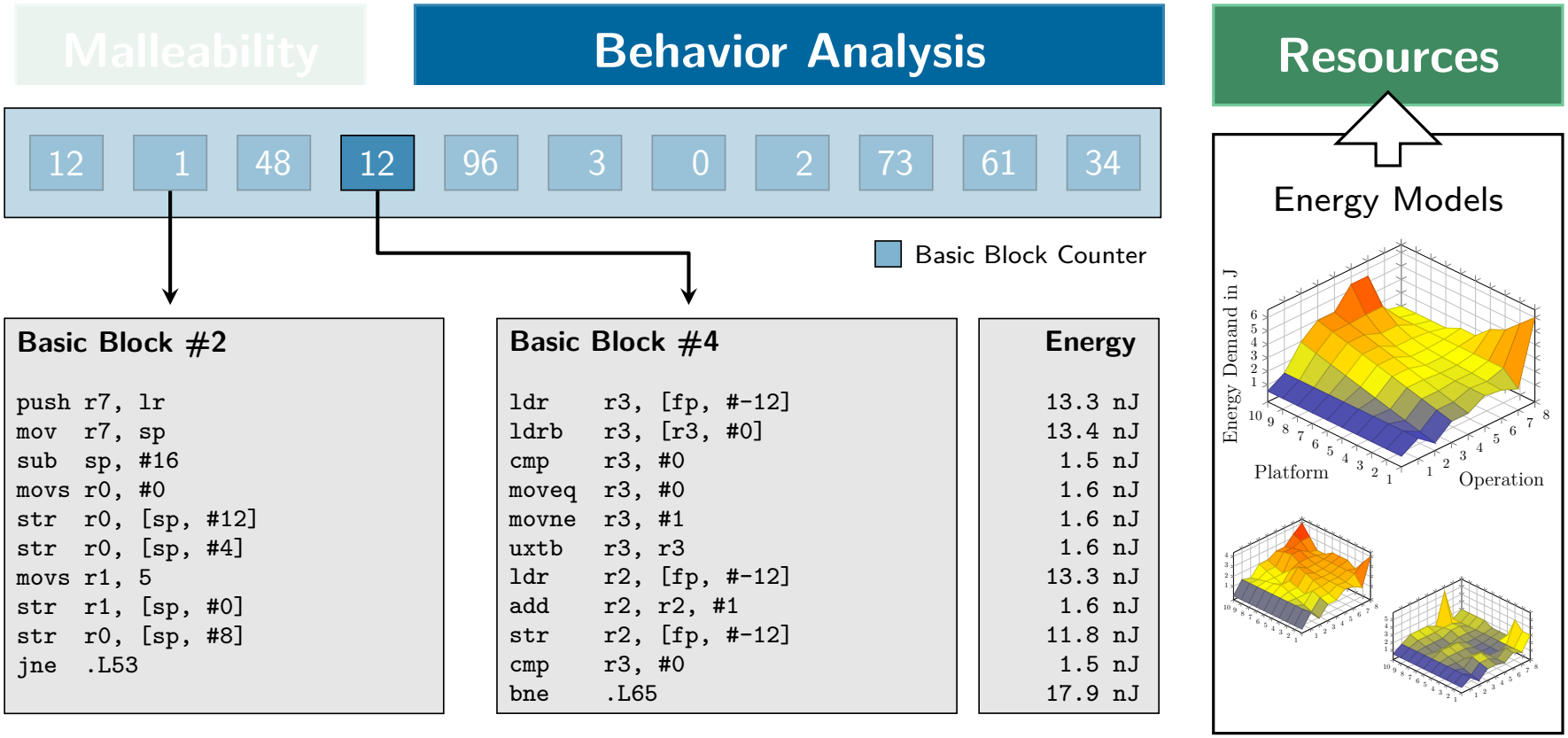

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HEAL: Program Example Fibonacci Sequence

Program:

```
1  # modes: (l)ookup, static
2  #        (c)alculate, dynamic
3  #        (m)emoisation, dynamic
4
5  def main():
6      mode = sys.argv[1]
7      fnum = 42
8
9      if mode == 'l':
10         fib_lookup(fnum)
11     elif mode == 'c':
12         fib_calc(fnum)
13     elif mode == 'm':
14         fib_calc_mem(fnum)
15
16 if __name__ == "__main__":
17     main()
```

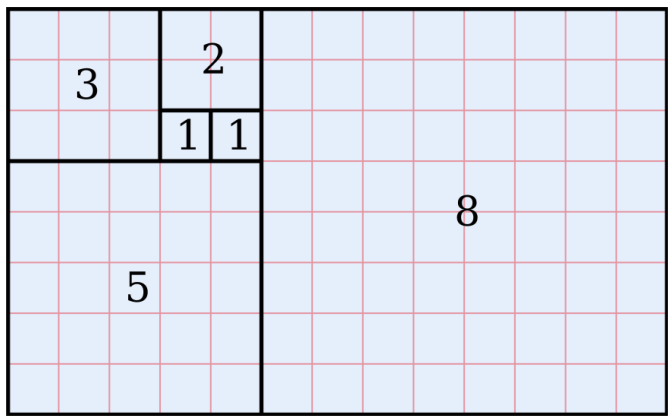
functionally identical

HEAL:

1. path exploration (`argv[1]`: symbolic)
2. generate program with concrete input

   z. B. `argv[1]: 'c', fnum: 42`
3. program execution and evaluation

→ energy demand estimate



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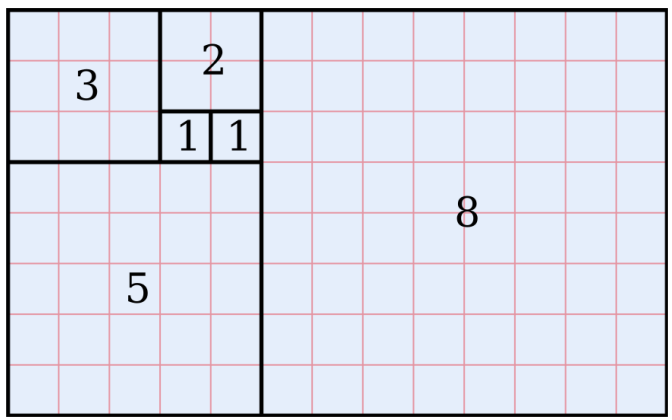

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$\rightarrow E_l$, $\rightarrow E_c$, $\rightarrow E_m$ — non-functionally different

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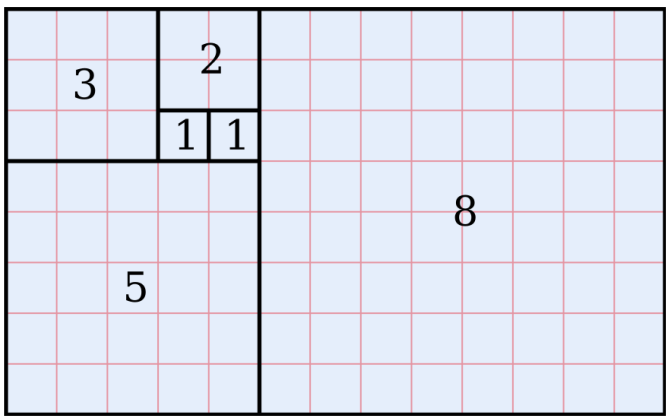
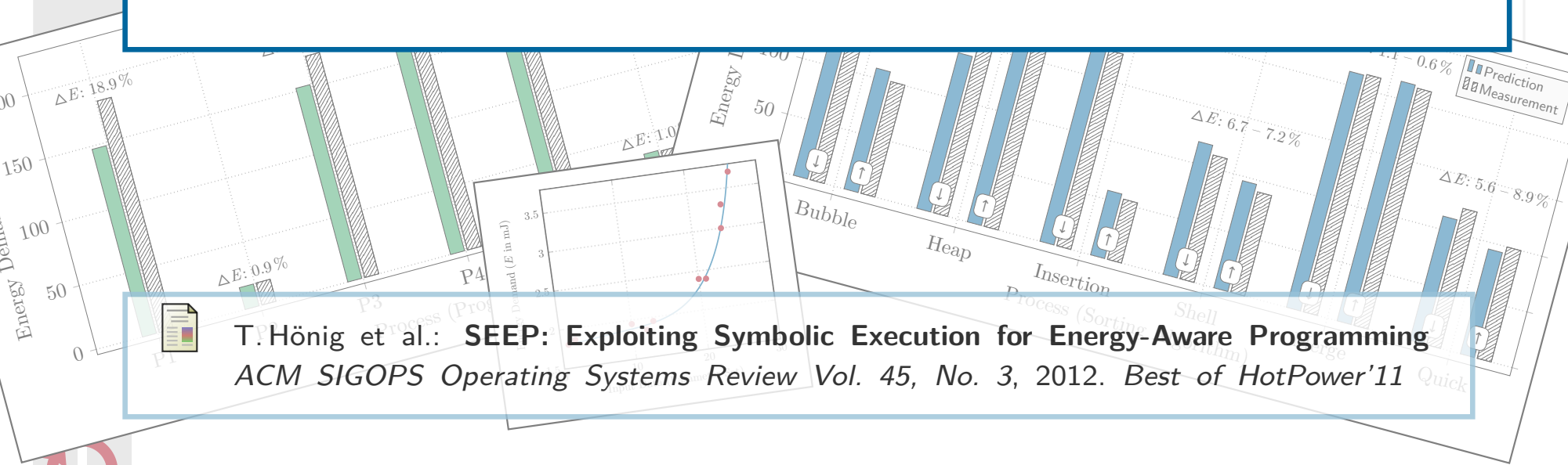


HEAL: Results and Open Questions

Malleability | Behavior Analysis | Resources

- energy demand estimates deviate on average by less than 9.1 % compared to energy measurements
- the evaluation shows that the energy demand of functionally identical processes deviate up to 3.9 times



T. Hönig et al.: **SEEP: Exploiting Symbolic Execution for Energy-Aware Programming** *ACM SIGOPS Operating Systems Review Vol. 45, No. 3*, 2012. *Best of HotPower'11*

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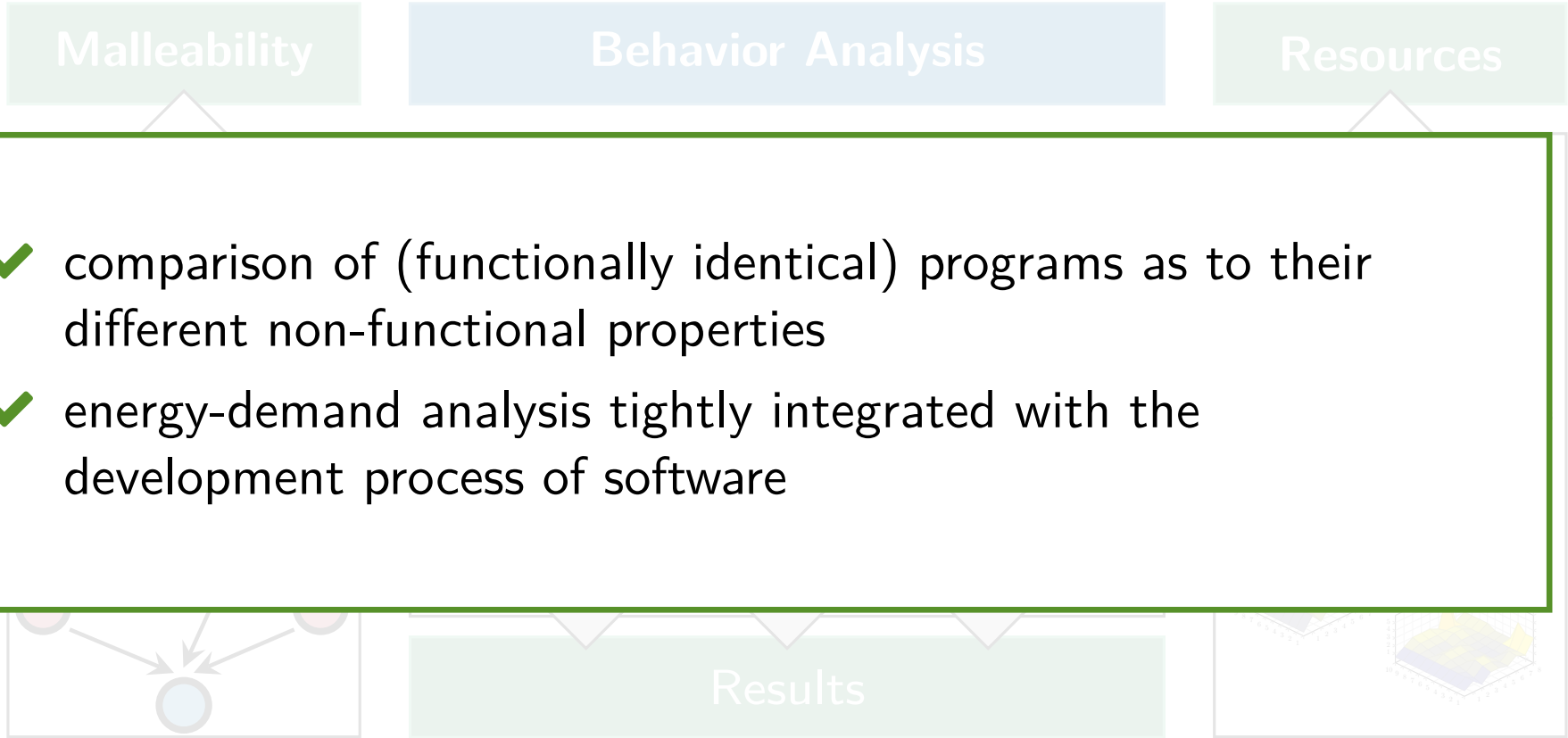

- ✔ comparison of (functionally identical) programs as to their different non-functional properties
- ✔ energy-demand analysis tightly integrated with the development process of software

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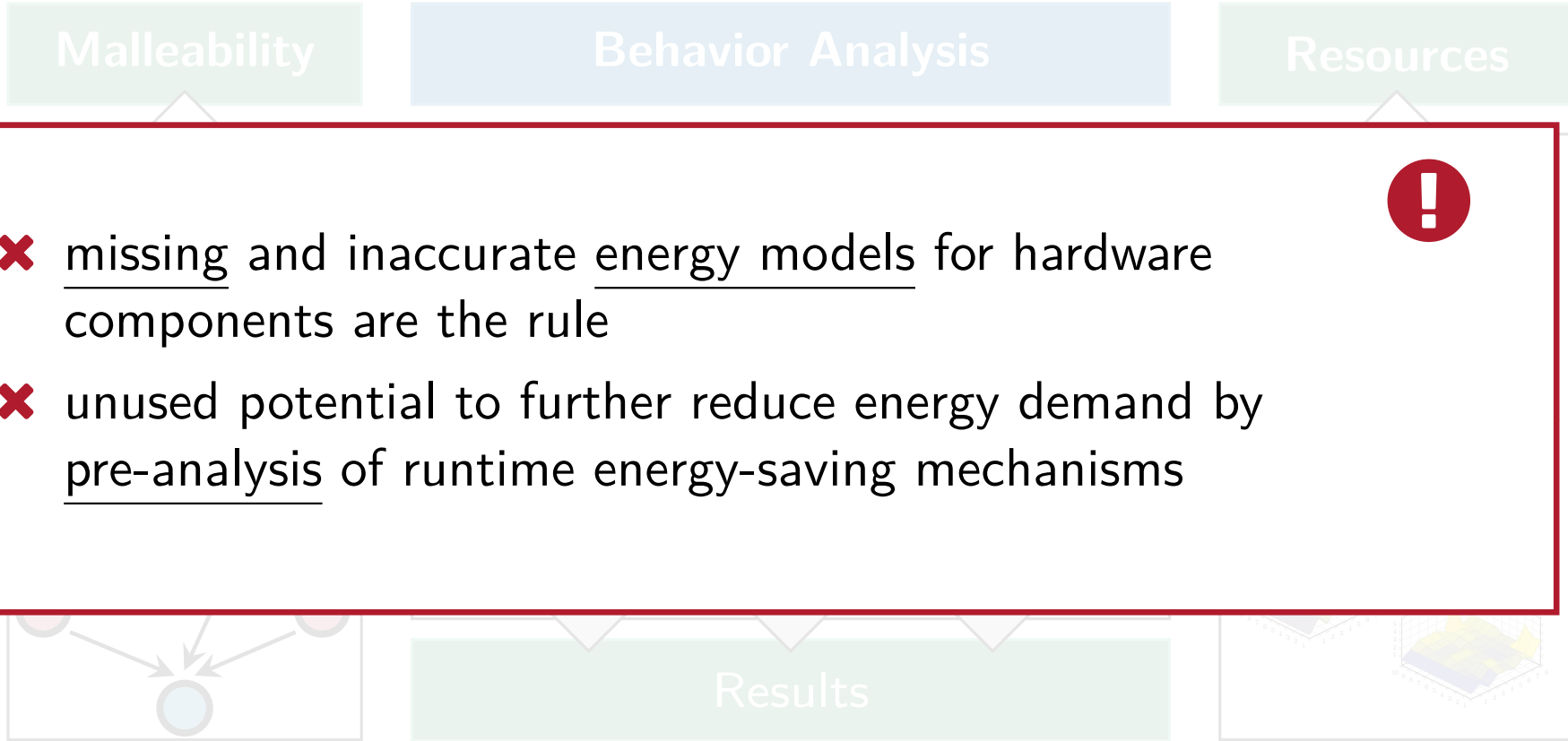
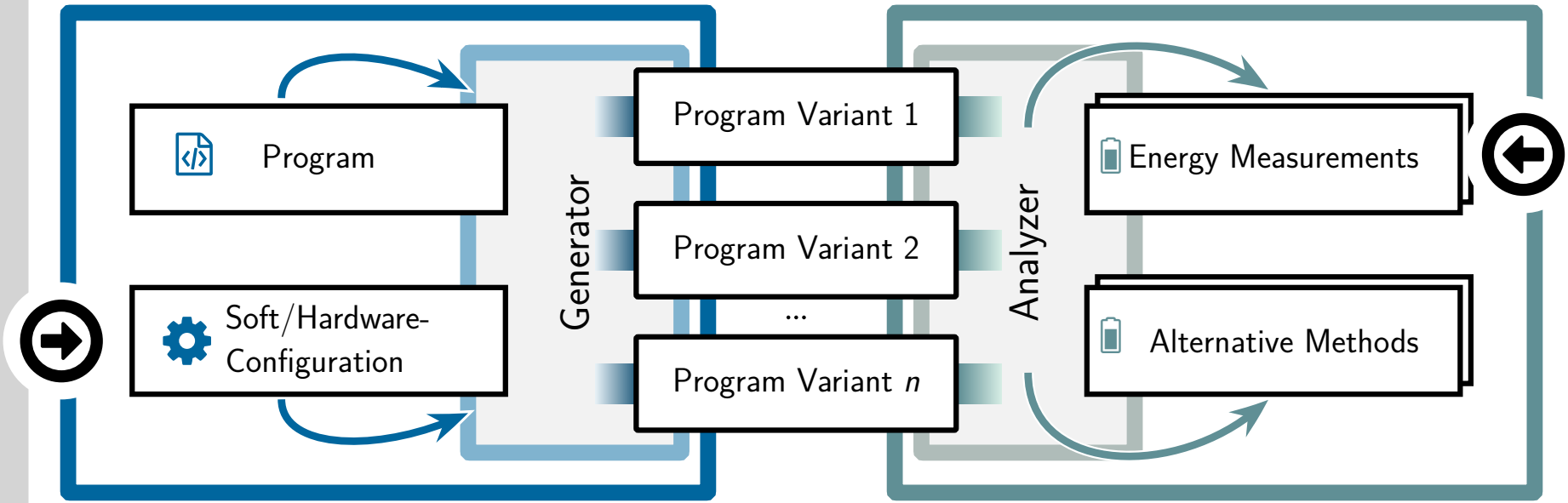


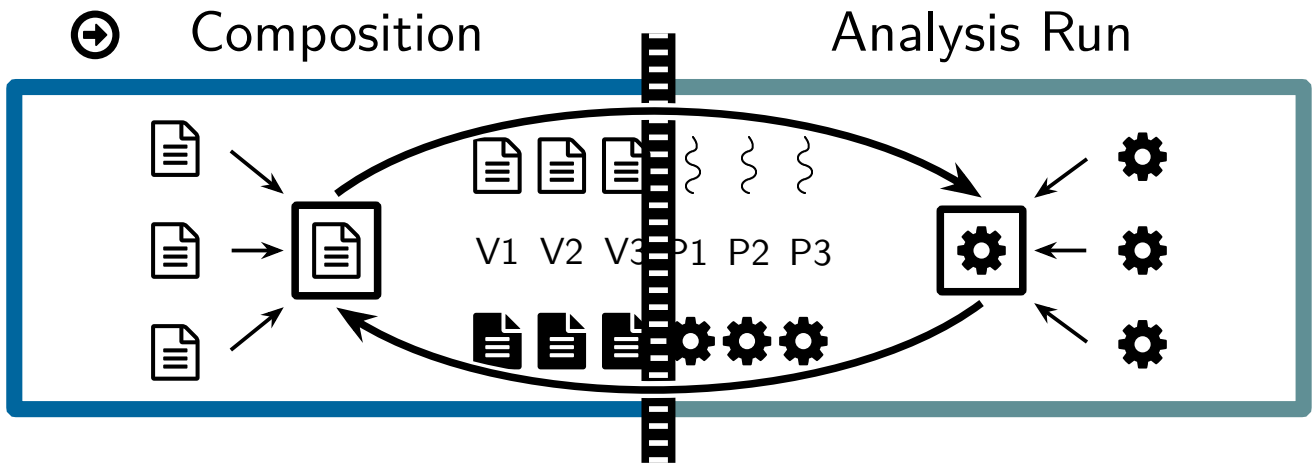
- ✖ missing and inaccurate energy models for hardware components are the rule
- ✖ unused potential to further reduce energy demand by pre-analysis of runtime energy-saving mechanisms

ROAM: Program Variant Generator and Analysis



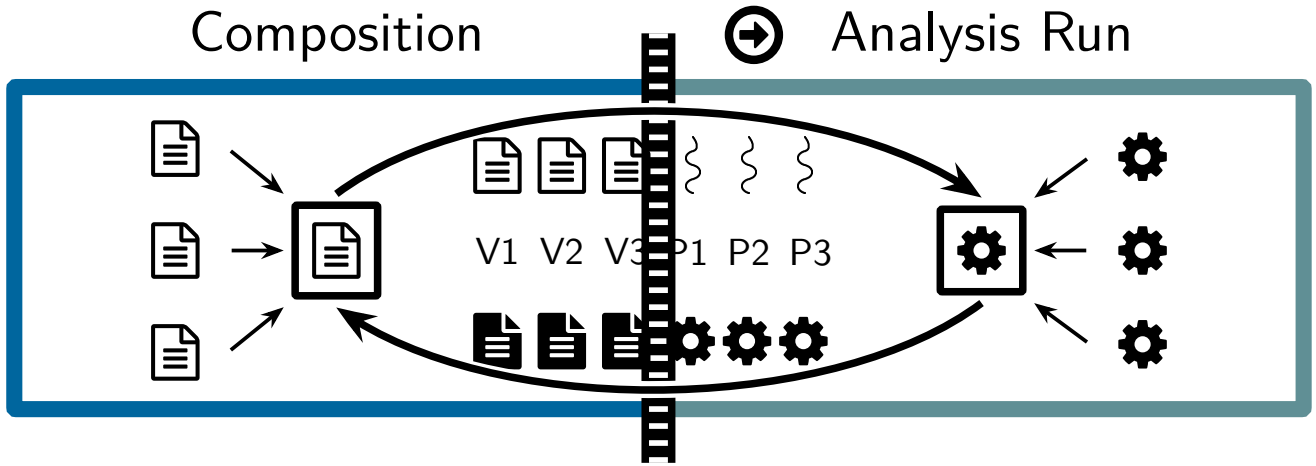
- generate program variants: programs with different software/hardware configurations
- energy measurements with a measuring circuit which is based on a current mirror for determining the energy demand

ROAM: Architecture and Implementation



- composition: static preparation for testing
  - heterogeneous hardware settings (z. B. energy saving features)
  - different software settings (z. B. compiler)
- analysis run: dynamic evaluation
  - execution of program variants on different hardware platforms
  - determination of execution time and energy demand by measurement

ROAM: Architecture and Implementation



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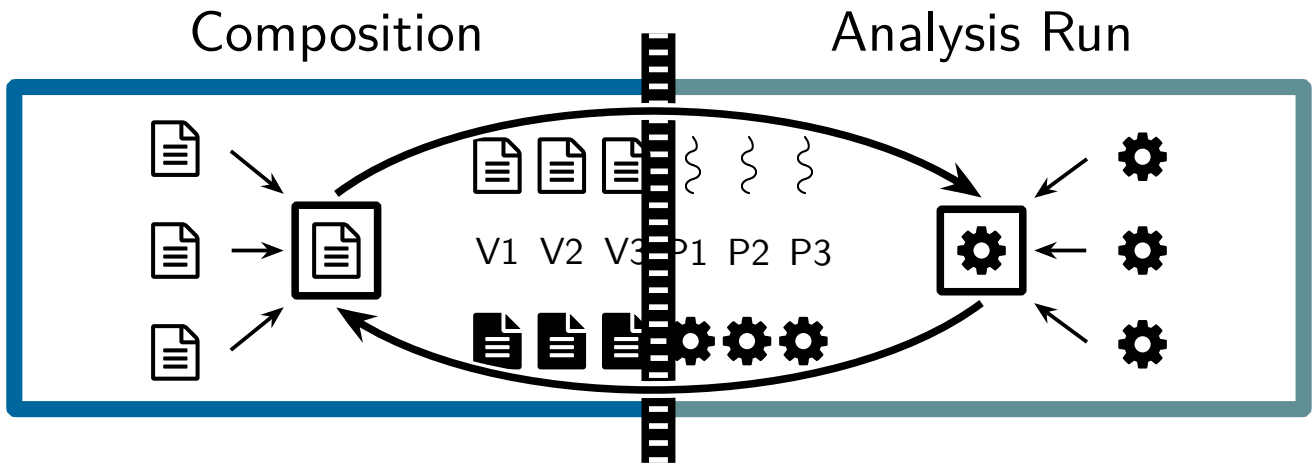


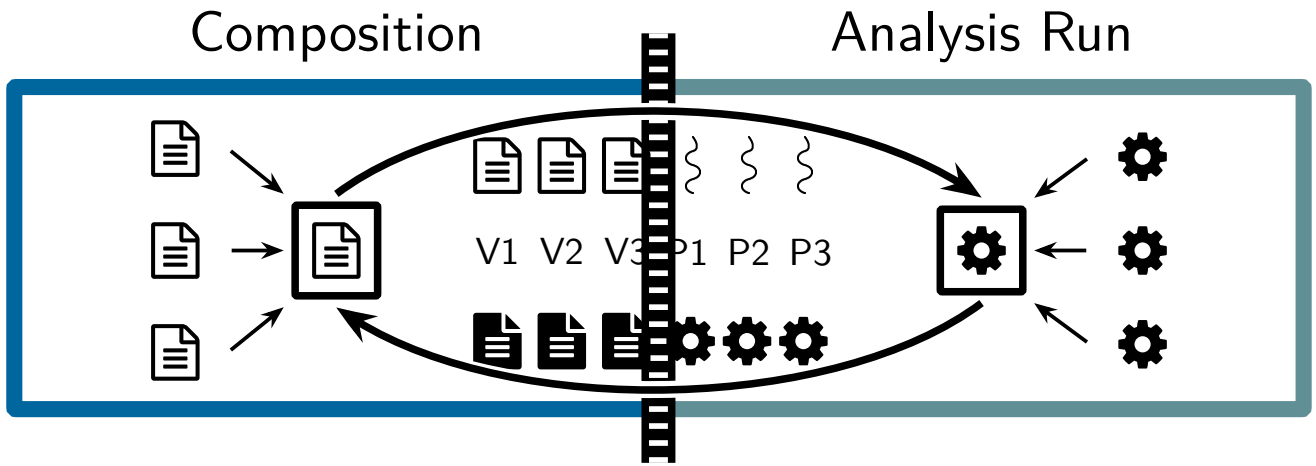
First experiment[1]: comparison of interface-compatible compilers

GCC vs. Clang
- in 80 % of the cases, GCC generates more energy-efficient program variants (up to a quarter lower energy demand)
- one program variant of Clang is approx. 10 x more energy-efficient than the corresponding variant of GCC

energy vs. time
- no causal relationship between process energy demand and execution time in 10 % of the program analyses

[1] Software: GNU GCC 4.8, LLVM Clang 3.4, Hardware: ARM Cortex-M0+ (Kinetis KL02)

ROAM: Experiments and Results



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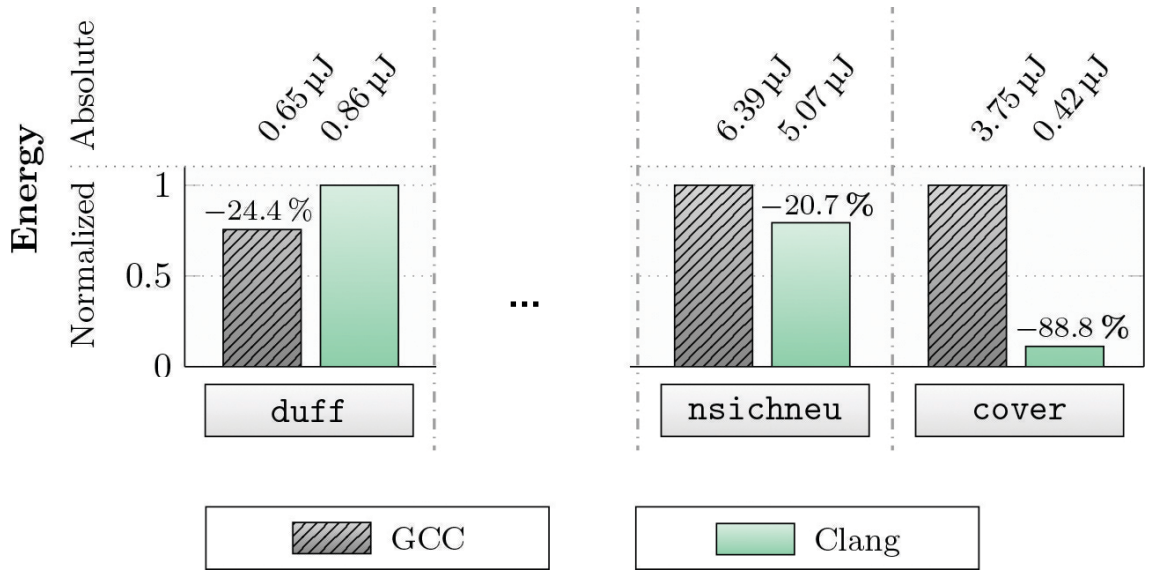

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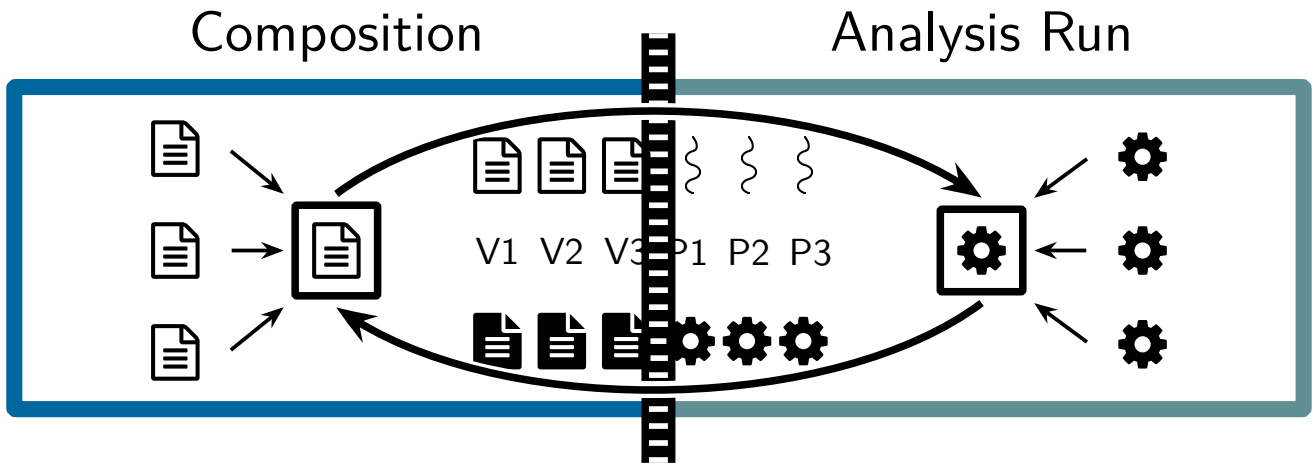

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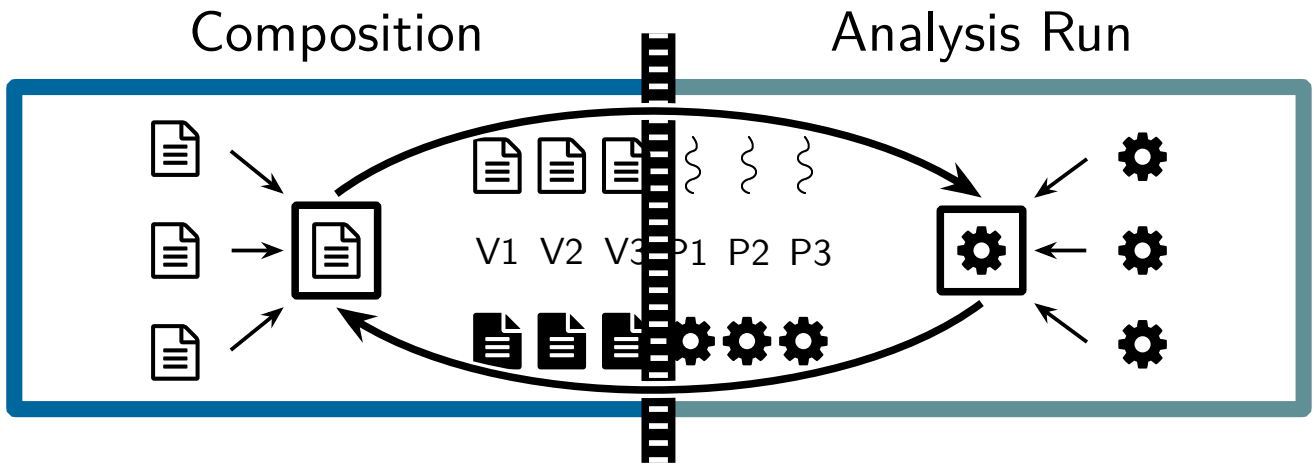
[1] Software: GNU GCC 4.8, LLVM Clang 3.4, Hardware: ARM Cortex-M0+ (Kinetis KL02)

ROAM: Experiments and Results



First experiment[1]: comparison of interface-compatible compilers

GCC vs. Clang
- in 80 % of the cases, GCC generates more energy-efficient program variants (up to a quarter lower energy demand)
- one program variant of Clang is approx. 10 x more energy-efficient than the corresponding variant of GCC

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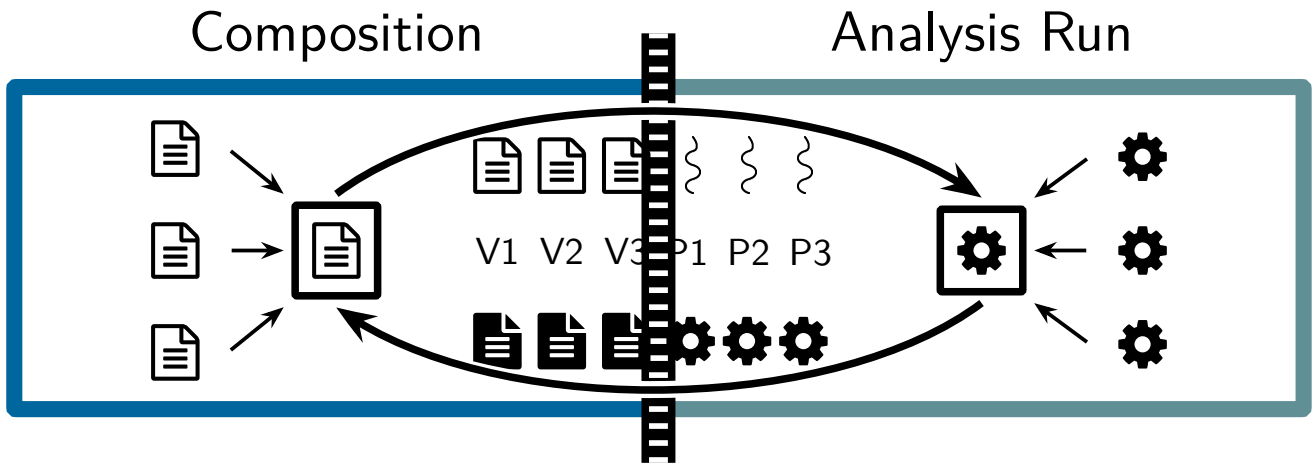
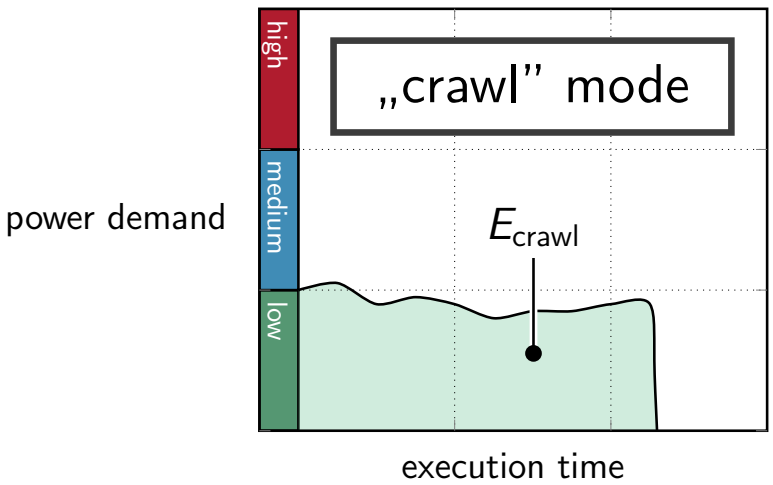
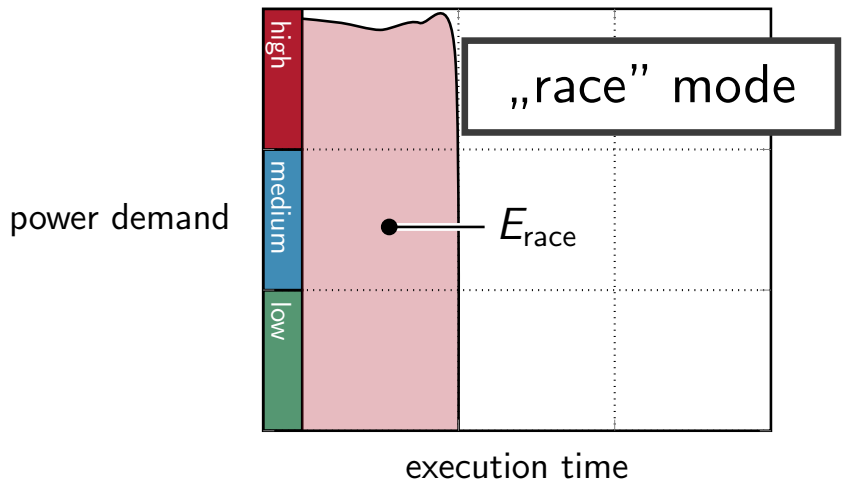


Second experiment[2]: scaling of operating voltage and clock frequency





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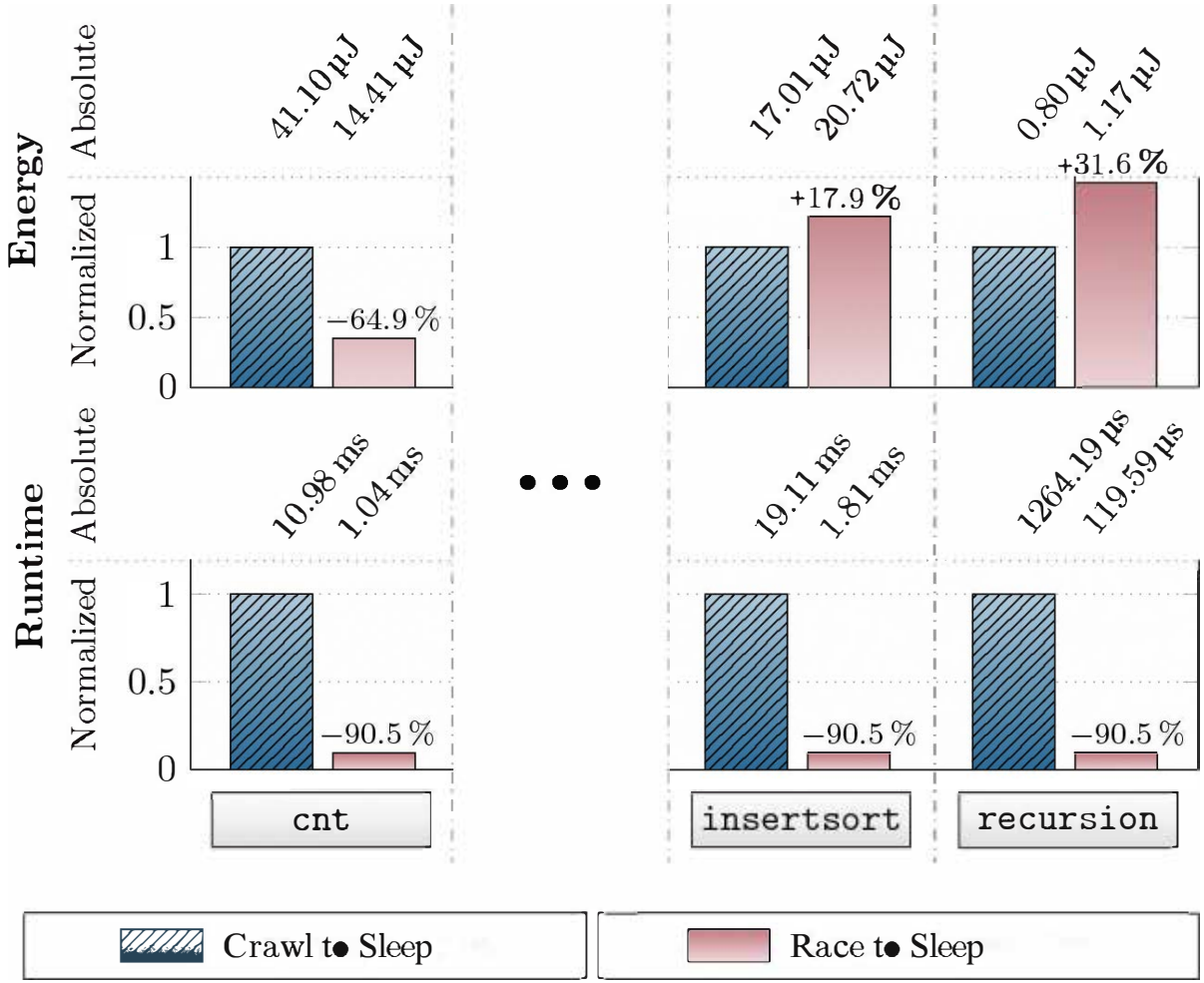

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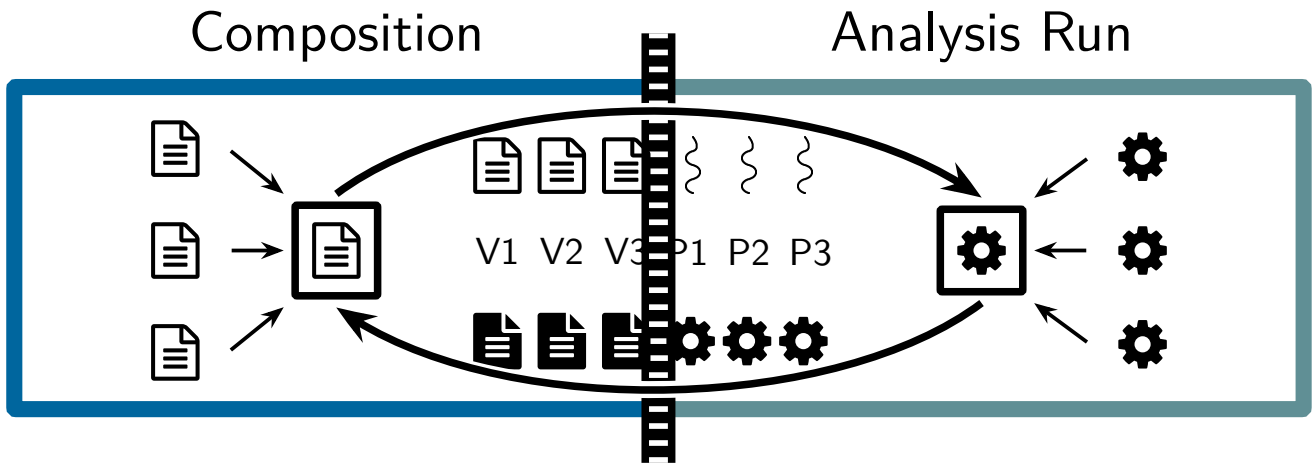

Second experiment[2]: scaling of operating voltage and clock frequency

race vs. craw
- „race" mode is commonly preferred to maximize idle time ($\rightarrow$ exploit sleep modes)
- expected increase in performance occurs in all test cases (i.e., shortening of the execution time)

energy vs. time
- however, no causal relationship between process energy demand and execution time in 20 % of the program analyses

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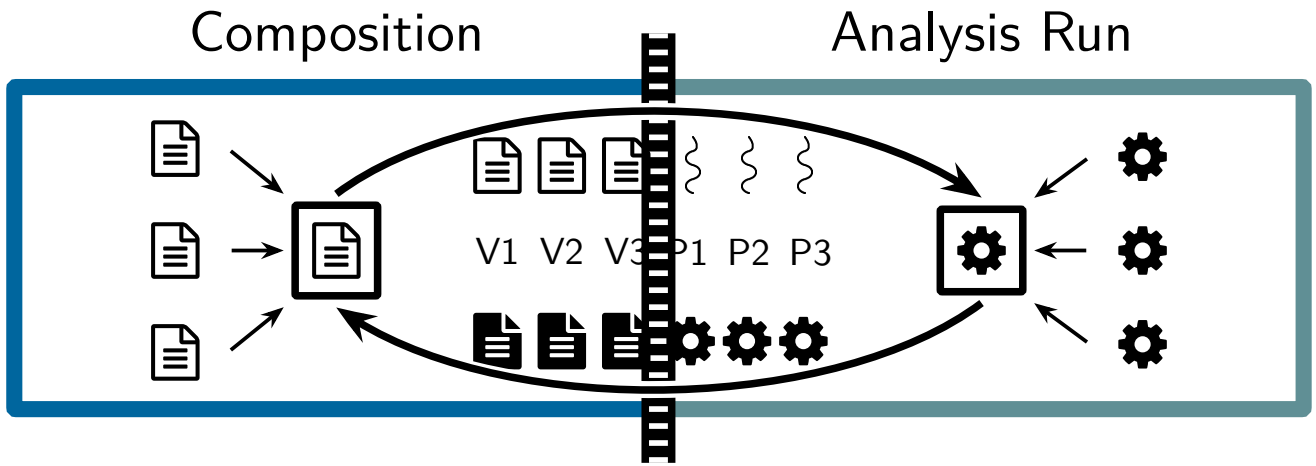

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ROAM: Program Example Fibonacci Sequence (II)

Program:

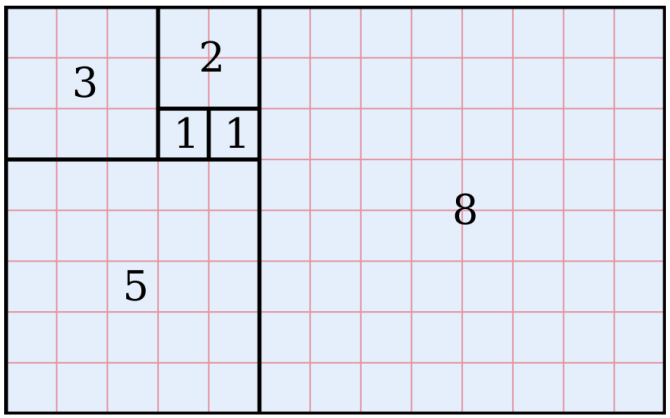
```
1  # modes: (l)ookup, static
2  #        (c)alculate, dynamic
3  #        (m)emoisation, dynamic
4
5  def main():
6      hwop = roam_fetch_hwops()
7      mode = sys.argv[1]
8      fnum = 42
9
10     sw_hardware_mode(hwop);
11     if mode == 'l':
12         fib_lookup(fnum)
13     elif mode == 'c':
14         fib_calc(fnum)
15     elif mode == 'm':
16         fib_calc_mem(fnum)
17     reset_hardware_mode();
18
19 if __name__ == "__main__":
20     main()
```

Run-Modes,
DVFS,
Timings
etc.

ROAM:

1. generate software and hardware settings to be used
2. generate program variants
3. process execution and evaluation

→ energy demand measurements
→ results evaluation



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Run-Modes, DVFS, Timings etc.

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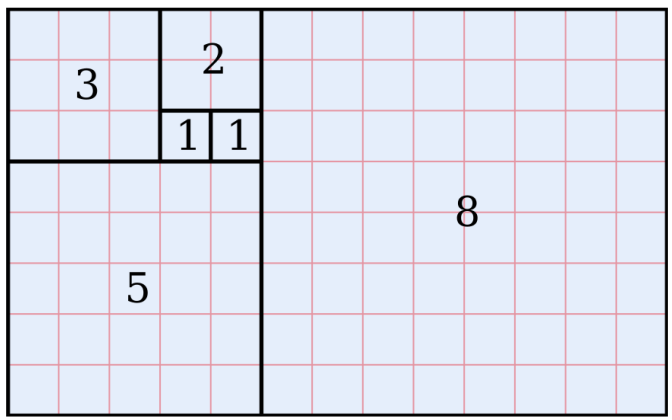

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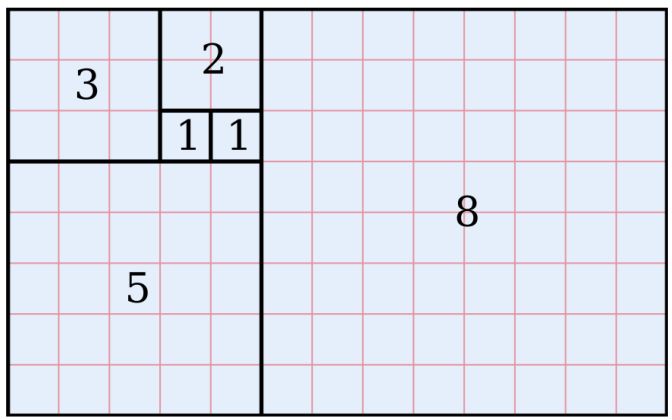
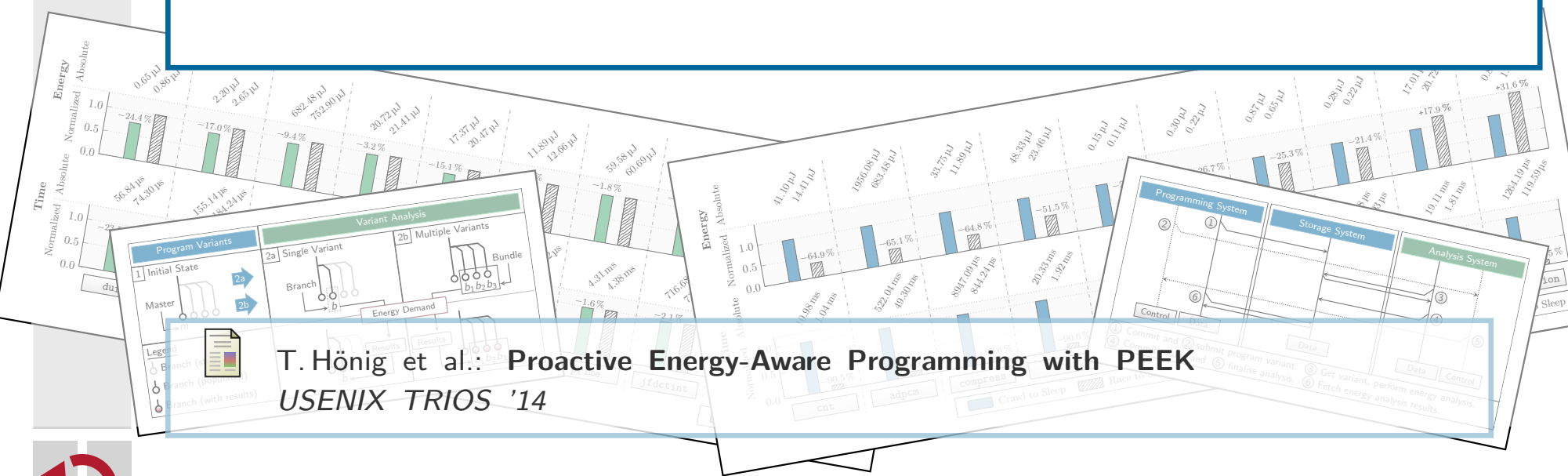


ROAM: Results

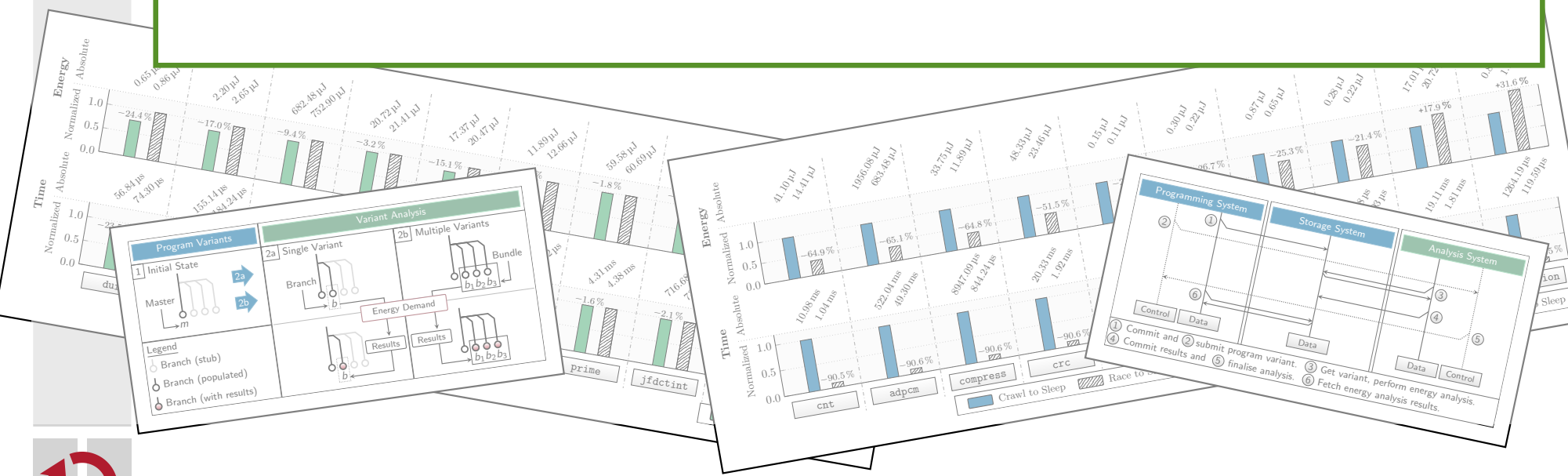
- configuration settings made by ROAM reduce hardware energy demand by between 18 % and 65 %
- choosing the right compiler infrastructure can reduce the energy demand by a factor of 10



T. Hönig et al.: **Proactive Energy-Aware Programming with PEEK**
USENIX TRIOS '14

ROAM: Results

- ✔ pre-analysis generates necessary a priori knowledge for suitable hardware settings at process execution time
- ✔ energy measurement during analysis addresses unavailability of energy models



Agenda

Paper Discussion

- paper discussion
  - R. Pereira et al.
    Energy efficiency across programming languages: how do energy, time, and memory relate?
    Proceedings of the 10th ACM SIGPLAN International Conference on Software Language Engineering (SLE'17), 2017.

Subject Matter

- energy-aware programming connects **static** (ahead of run time) with **dynamic** (at run time) analysis
- use **cross-layer considerations** to reduce energy demand
- pinpoint relevant program code sections for **extended analysis** and manual labor
- reading list for Lecture 10:
  - X. Fan et al.
    Power provisioning for a warehouse-sized computer
    Proceedings of the 34th International Symposium on Computer architecture (ISCA'07), 2007.

Reference List I

[1] Hönig, T. ; Eibel, C. ; Kapitza, R. ; Schröder-Preikschat, W. :
SEEP: exploiting symbolic execution for energy-aware programming.
In: *Proceedings of the 2011 Workshop on Power-Aware Computing and Systems (HotPower '11)* ACM, 2011, S. 17–22. –
Best of HotPower 2011 Award.

[2] Hönig, T. ; Janker, H. ; Eibel, C. ; Mihelic, O. ; Kapitza, R. ; Schröder-Preikschat, W. :
Proactive Energy-Aware Programming with PEEK.
In: *Proceedings of the 2014 Conference on Timely Results in Operating Systems (TRIOS '14)* USENIX, 2014, S. 1–14