

# Concurrent Systems

*Nebenläufige Systeme*

## II. Concurrency

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November 6, 2019



# Agenda

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Preface

Causality

Interdependencies

Dimensions

Resource Sharing

Principles

Competition

Synchronisation

Summary



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## Causality

- Interdependencies

- Dimensions

## Resource Sharing

- Principles

- Competition

- Synchronisation

## Summary



- discussion on two fundamental **abstract concepts**:
  - concurrency (Ger. *Nebenläufigkeit*),
    - designates the relation of causal independent events
    - is related to events that have no mutual influence
  - causality (Ger. *Kausalität, Ursächlichkeit*)
    - designates the relation between cause and effect
    - is the causal chain or connection of two events



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Events occur or are concurrent if none is the cause of the other.



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## Definition (concurrent)

Events occur or are concurrent if none is the cause of the other.

- explanation of the relation of these concepts to **resource sharing**
  - differentiated with respect to various types of resources and sharing
  - classified as to appropriate or necessary synchronisation paradigms



# Outline

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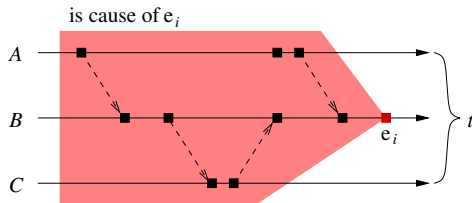
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# Principle of Causality

- causal chain of events related to some other event  $e_i$ :



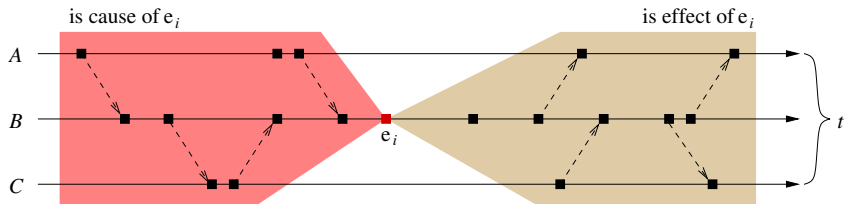
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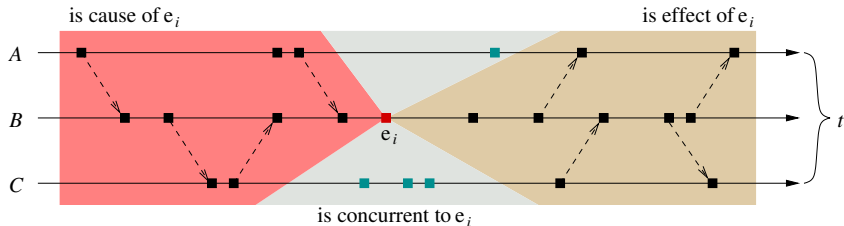


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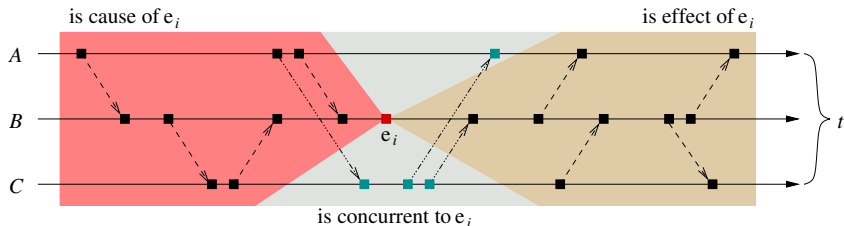


- $A$ ,  $B$  and  $C$  denote some computation on a private or shared processor
- an event is concurrent to another event ( $e_i$ ) if it lies in the elsewhere of the other event ( $e_i$ )
- the event is neither cause nor effect of the other event ( $e_i$ )



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- the event is neither cause nor effect of the other event ( $e_i$ )
  - as the case may be, it is cause/effect of other events (different from  $e_i$ ) that are lying in the elsewhere (cf. dash-and-dot line)



# Order of Precedence

- computations can be carried out concurrently provided that:
  - general ■ none requires a result of the other (cf. p. 10)
  - non-existent **data dependencies**



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  - special** ■ none depends on delays brought forth by the other
    - deadlines may be missed rarely or under no circumstances
    - periods may be stretched up to a certain limit or not at any time
  - non-existent **timing restrictions**  $\leadsto$  *real-time processing*



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- interrelation of computations/events constrains concurrency

## Event correlations v. Processing modes

$\left. \begin{array}{l} \text{"is cause of"} \\ \text{"is effect of"} \end{array} \right\} \mapsto \text{sequential} \text{ (realised before/at run-time)}$

$\text{"is concurrent to"} \mapsto \text{parallel} \text{ (realised in logical/real terms)}$



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“is concurrent to”  $\mapsto$  **parallel** (realised in logical/real terms)

$\hookrightarrow$  decrease of the portion of **sequential code** is an important aspect



# Limits in the Degree of Concurrency

---

- Amdahl's Law [1]: speed-up ( $su$ ) achievable by parallel processors
  - work load remains constant with the varying number of processors
  - aim at reducing overall computation time for a given fixed-size problem





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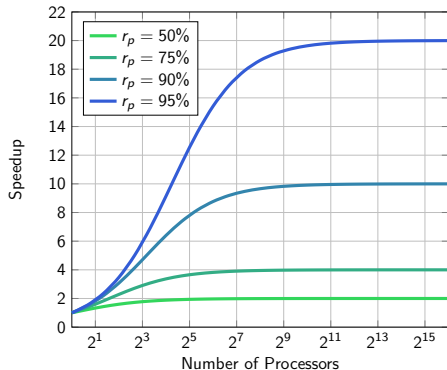
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$$su = (r_s + r_p) / (r_s + \frac{r_p}{n})$$
$$= \frac{1}{r_s + \frac{r_p}{n}}$$

$r_s$  ratio of sequential code

$r_p$  ratio of parallel code,  
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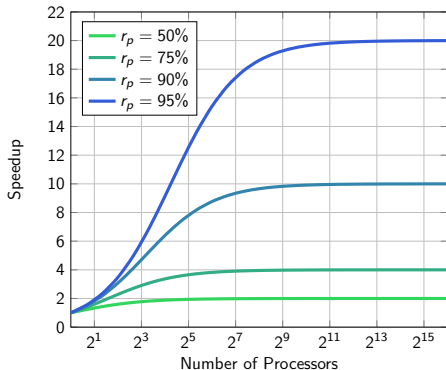
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- speed-up will be constrained by **data management housekeeping**
  - the nature of this overhead appears to be sequential



# Adapting the Work Load

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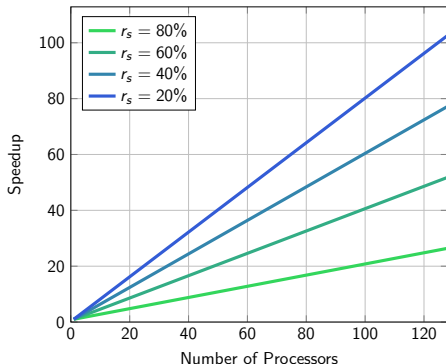
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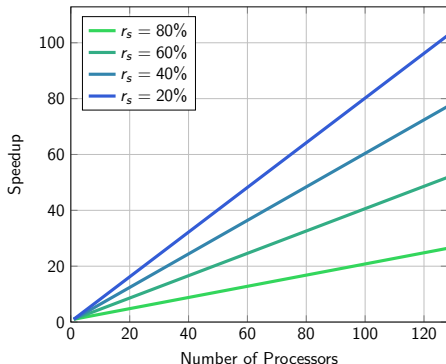
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- data management housekeeping (serial part) becomes less important
  - in practise, the problem size scales with the number of processors: **HPC**<sup>1</sup>

<sup>1</sup>High Performance Computing



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6      foo = tupel[0];
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9      subtotal = foo + bar;
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in computation:

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  - 6 and 7
  - 9 and 10
- which statements are not concurrent?
  - (6, 7) and (9, 10)
  - (9, 10) and 12



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- defined by the **causal order** (Ger. *Kausalordnung*) of the statements
  - as far as the logical dimension of a program is concerned



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  - (9, 10) and 12
- defined by the **causal order** (Ger. *Kausalordnung*) of the statements
  - as far as the **logical dimension** of a program is concerned
  - but there is also a **physical dimension**, namely when it comes to the execution of that program by a real processor  $\leadsto$  *level of abstraction*



# Level of Abstraction

---

- a concurrent operation (in logical terms) at a higher level can be sequential (in real terms) at a lower level
  
- a sequential operation (in logical terms) at a higher level can be “concurrent” (i.e., **non-sequential** in real terms) at a lower level



- a concurrent operation (in logical terms) at a higher level can be sequential (in real terms) at a lower level
  - the operation handles a resource that can be used only consecutively
    - a single memory area that is shared by multiple computations
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- ⇒ may result in a *performance penalty*, non-critical situation



- a sequential operation (in logical terms) at a higher level can be “concurrent” (i.e., **non-sequential** in real terms) at a lower level
  - the operation appears to be complex, consists of multiple sub-steps
    - the  $n$ -bit assignment on a  $\frac{n}{2}$ -bit machine, with  $n = 16, 32, 64$
    - the addition of a number to a shared variable located in main memory
  - simultaneous execution of the sub-steps must be considered (cf. p. 18)



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- may result in a *performance penalty*, non-critical situation **but for...**<sup>2</sup>
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- reveals a **race condition**, substantial critical situation: **error**

<sup>2</sup>real-time processing, especially in case of hard deadlines.



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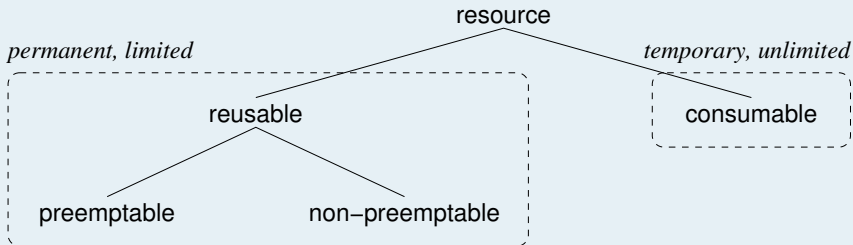
Principles

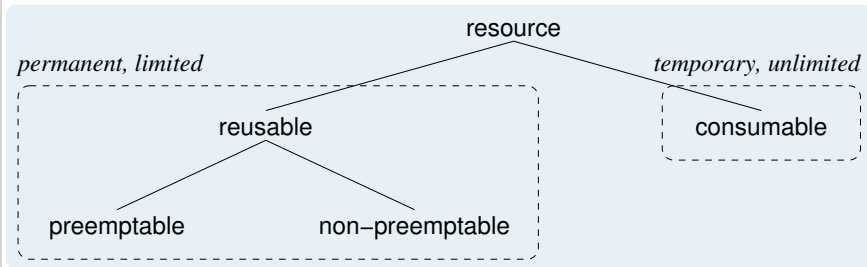
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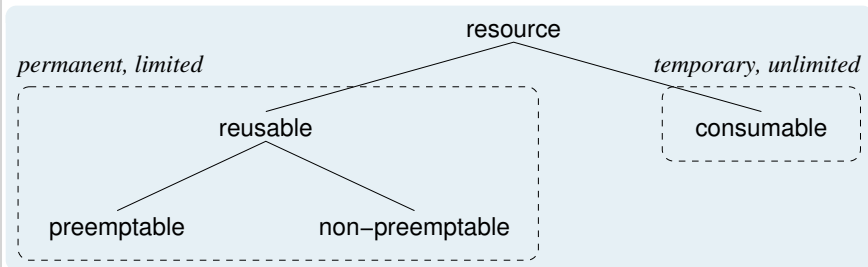




- permanent<sup>3</sup> resources are **reusable**, but always only of limited supply
  - they are acquired, occupied, used, and released (when no longer required)
    - in-use resources are preemptable or non-preemptable, depending on whether allocation to another occupant is possible
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- temporary resources are of unlimited supply, they are **consumable**
  - i.e. produced, received, used, and destroyed (when no longer required)

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# Resource Peculiarities

- **hardware resources** as to be managed, e.g., by an operating system

## reusable

- processor ■ CPU, FPU, GPU; MMU
- memory ■ RAM, scratch pad, flash
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## consumable

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- reusable data resources are notably **container** for consumable resources
  - the latter must be contained in variables/placeholders to be processible
- availability of the former constrains production/consumption of the latter





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- reusable and consumable resources imply different **use patterns**



# Resource Use Patterns

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- if so, **reusable resources** are subject to **multilateral** synchronisation
- **consumable resources** are subject to **unilateral** synchronisation



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  - provided that the following two basic conditions (i.e., constraints) apply:
    - i resource accesses by computations may happen (quasi-) simultaneously
    - ii simultaneous accesses may cause a **conflicting state change** of the resource
  - simultaneous use of a **shared resource** this way must be coordinated
    - coordination may affect computations in a blocking or non-blocking manner<sup>4</sup>

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- **consumable resources** are subject to **unilateral** synchronisation
    - generally also referred to as logical or conditional synchronisation:
      - logical – as indicated by the “role playing” of the involved computations
      - conditional – as indicated by a condition for making computational progress
    - use of a **temporary resource** follows a causal course of events or actions
      - by affecting producers in a non-blocking and consumers in a blocking way
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  - use of a **temporary resource** follows a causal course of events or actions
    - by affecting producers in a non-blocking and consumers in a blocking way
- simultaneous computations **overlap** in time, interfere with each other
  - they become critical in any case if they also overlap in (identical) place

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- assuming that the following subroutines (put and get) are executed in any order and that they may also run simultaneously:

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1 char buffer[80];
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4 void put(char item) {
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- values may be read from an empty buffer: **underflow**

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  - **overlapping auto-increments** may manifest wrong values



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- put and get must be subject to uni- and multilateral synchronisation
  - they are not concurrent under the assumption that was made above

- which logical problems exist?
  - buffered items may be overwritten: **overflow**
  - values may be read from an empty buffer: **underflow**
- which other problems exist?
  - **overlapping writes** may go to the same memory location
  - similar to **overlapping reads**, but reverse
  - **overlapping auto-increments** may manifest wrong values



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  - off-line
    - static scheduling based on control-flow and data dependencies
    - *analytical approach* that takes *a priori* knowledge as given (v.s. i)
    - at run-time, dependable operations are implicitly synchronised



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    - based on either pessimistic or optimistic run-time assumptions
- the chosen synchronisation method should be *minimally invasive*



# Divisibility in Temporal Respect

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  - as compiled from C to ASM (x86): `gcc -O3 -m32 -static -S`

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- the critical case may result in **wrong reading** (Ger. *Zählerwert*) of `in/out`
  - `in++` or `out++` are not concurrent to oneself, resp.: they are **not re-entrant**

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    - synchronised operation is made of sequential code
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    - allow potential overlapping, achieve consistency afterwards
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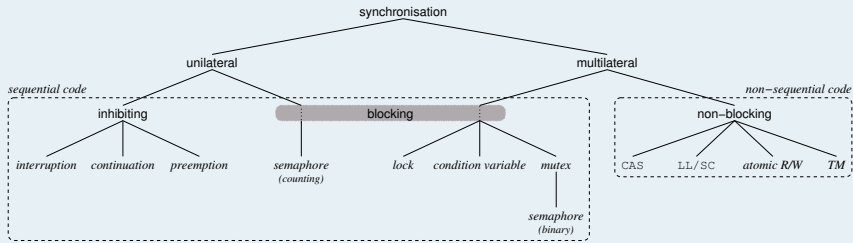


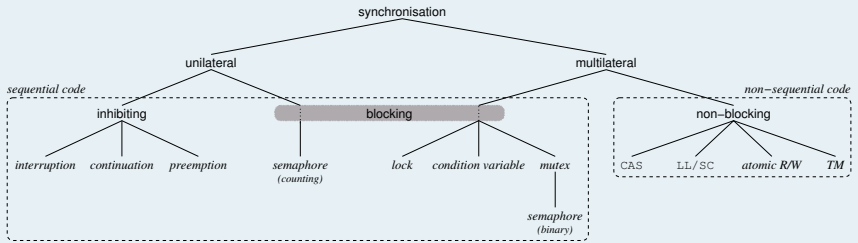
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- both approaches come in a variety of solutions to the same problem

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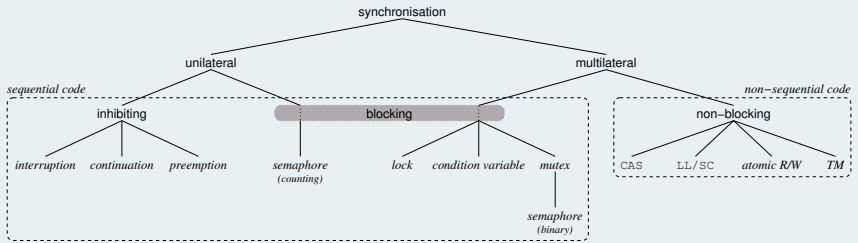






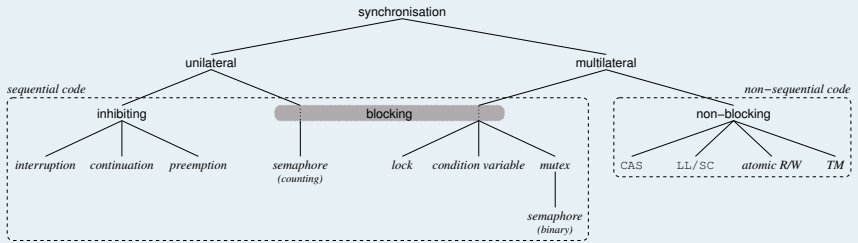
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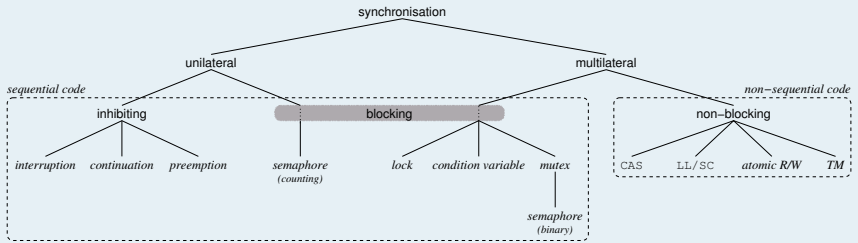




- the methods are more or less disruptive of the problematic operation:

non-sequential ■ reprogram sequential code as a **transaction** 😊  
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    - ↪ *pessimistic*, overlapping is not a rare event
  - non-sequential** ■ reprogram sequential code as a **transaction** 😊
    - for the most part, the original code cannot be reused 😞
    - ↪ *optimistic*, overlapping is a rare event
- wherever applicable, **downsizing sequential code** is basic
  - i.a. Amdahl's Law (cf. p.8) argues for non-blocking synchronisation



# Synchronisation Behaviour

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- effect of synchronisation procedures on the computations involved



- effect of synchronisation procedures on the computations involved:
  - inhibiting ■ prevents other computations from launching
    - irrespective of the eventuality of co-occurrence
    - applies to consumable resources (i.e., signals), only
  - running computations are not delayed



- effect of synchronisation procedures on the computations involved:

- blocking
  - delays computations subject to resource availability
    - takes effect only in case of co-occurrence (overlapping)
    - applies to reusable and consumable resources
  - running computations are possibly delayed





- effect of synchronisation procedures on the computations involved:

- non-blocking
- may force non-dominantly running computations to repeat
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- it bears repeating: ***downsizing sequential code*** is basic
  - where possible, non-blocking synchronisation should be the first choice
- but even then: there is no all-in-one approach for every purpose. . .



# Outline

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Preface

Causality

Interdependencies

Dimensions

Resource Sharing

Principles

Competition

Synchronisation

Summary



- understanding (Ger.) ***Gleichzeitigkeit*** in its various meanings



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  - concurrency ■ happening together in time and place [7]
  - designates the relation of causal independent events
  - when none computation depends on results of the other



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- occurring, done, existing together or at the same time [7]
- effect of a certain operation mode of a computing machine
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  - concurrency implies unconstrained overlapping in time and place
  - but simultaneity may also cause overlapping that must be constrained
- synchronism ensures that overlapped complex operations do right
  - the individual sub-steps will be strictly executed *interim* (consecutively) or
  - a *transaction* will take care for consistent (pseudo-) parallel execution



- the concept of (distant) simultaneity is not absolute, but depends on the **frame of reference** (Ger. *Bezugssystem*) an observer takes
  - moving- and fixed-platform thought experiment [2, p. 768]:

*The simultaneity of two distant events means a different thing to two different observers if they are moving with respect to each other.*



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    - while multiple invocations of the former will take place sequentially,<sup>7</sup> the corresponding ones of the latter may come about non-sequentially
    - while multiple invocations of the latter discretely can be concurrent, their logical correlation to the former makes them possibly not concurrent

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    - while multiple invocations of the latter discretely can be concurrent, their logical correlation to the former makes them possibly not concurrent
  - operations must be resolved **cross-level** (from “fixed platform” observed) in order to realise their ability for concurrency or need for synchronism

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- computations can be **concurrent** if none needs a result of the other
  - they must be free of data and control-flow dependencies
- in order to be concurrent, computations must be **simultaneous**
  - quasi-simultaneous through partial virtualisation (hardware multiplexing) or real simultaneous by multiprocessing (hardware multiplication)
  - both techniques will induce computations to overlap in time and place
- **overlapping** in time cause interference but is the lesser of two evils
  - more critical is overlapping **in place** relating to the same resource
  - particularly with regard to the same (i.e., shared) memory area
- critical overlapping must be counteracted through **synchronisation**
  - i.e., coordination of the cooperation and competition between processes
  - here: uni- or multilateral synchronisation, depending on the resource type
- synchronisation ensures for **indivisibility** of a computation cycle
  - at the outset: physical, in blocking manner, by being pessimistic ☹️
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# Reference List I

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- [1] AMDAHL, G. M.:  
Validity of the Single-Processor Approach to Achieving Large Scale Computing Capabilities.  
In: *Proceedings of the AFIPS Spring Joint Computer Conference (AFIPS 1967 (Spring))*, AFIPS Press, 1967, S. 483–485
  
- [2] COMSTOCK, D. F.:  
The Principle of Relativity.  
In: *Science* 31 (1910), Mai 20, Nr. 803, S. 767–772.  
<http://dx.doi.org/10.1126/science.31.803.767>. –  
DOI 10.1126/science.31.803.767
  
- [3] DIJKSTRA, E. W.:  
Cooperating Sequential Processes / Technische Universiteit Eindhoven.  
Eindhoven, The Netherlands, 1965 (EWD-123). –  
Forschungsbericht. –  
(Reprinted in *Great Papers in Computer Science*, P. Laplante, ed., IEEE Press, New York, NY, 1996)
  
- [4] GUSTAFSON, J. L.:  
Reevaluating Amdahl's Law.  
In: *Communications of the ACM* 31 (1988), Mai, Nr. 5, S. 532–533



- [5] HOLT, R. C.:  
*On Deadlock in Computer Systems.*  
Ithaca, NY, USA, Cornell University, Diss., 1971
- [6] HOLT, R. C.:  
Some Deadlock Properties of Computer Systems.  
In: *ACM Computing Surveys* 4 (1972), Sept., Nr. 3, S. 179–196
- [7] NEUFELDT, V. (Hrsg.) ; GURALNIK, D. A. (Hrsg.):  
*Webster's New World Dictionary.*  
Simon & Schuster, Inc., 1988



- **bounded buffer** using a counting semaphore [3] for unilateral and an ELOP (x86) for multilateral synchronisation

```
1  typedef int semaphore_t;
2
3  extern void P(semaphore_t*);
4  extern void V(semaphore_t*);
5
6  semaphore_t free = 80;
7  semaphore_t empty = 0;
8
9  static inline int fai(int *ref) {
10     int aux = 1;
11
12     asm volatile("lock; xaddl %0,%1"
13         : "=r" (aux), "=m" (*ref)
14         : "0" (aux), "m" (*ref));
15
16     return aux;
17 }
18
19 char buffer[80];
20 unsigned in = 0, out = 0;
21
22 void put(char item) {
23     P(&free);
24     buffer[fai(&in) % 80] = item;
25     V(&empty);
26 }
27
28 char get() {
29     char item;
30
31     P(&empty);
32     item = buffer[fai(&out) % 80];
33     V(&free);
34
35     return item;
36 }
```

**free** ■ controls the number of unused buffer entries

■ *P* prevents from buffer overflow, *V* signals reusable resource

**empty** ■ controls the number of used buffer entries

■ *P* prevents from buffer underflow, *V* signals consumable resource

**fai** ■ indivisibly *fetch and increment* specified counter variable

